ICOS FINAL EVENT

Results & Recommandations on International Cooperation on Semiconductors for European Economic Resilience

3rd February 2026,
The Faculty, Brussels

Side event of
ECS BROKERAGE EVENT 2026
4 & 5 February
The EGG, Brussels

Morning Session

8:15 Welcome Coffee & Registration

9:00 - Welcome address: Pierre Chastanet Head of Unit - Microelectronics and Photonics Industry, European Commission - DG Connect

9:10 Introduction and Key Results of International Cooperation on Semiconductors for European Economic Resilience: Francis Balestra, ICOS Coordinator, Grenoble INP-UGA/CNRS

9:40 Economic Analysis of Semiconductor Value Chain

- **9:35 – 10:40 Analysis of the International Economic Landscape to Identify Cooperation Opportunities: Léo Saint-Martin – DECISION Etudes & Conseil**

10:45 – 11:15 Coffee-break

11:15 Pitches and Panel Discussion: "Foundation of international Cooperation: Analyses and Promising Path"

- **11:15 – 12:20 Pitches: From the technology landscape to cooperation: How to apply filters for priorities Ryoichi Ishihara – TU Delft, Feedback Loop with the Community: Recommendation for International Cooperation in R&D: Peter Ramm – Fraunhofer, International Cooperation: Which Paths Are Most Promising? Melanie Hentsche**

- **Panel Foundation of International Cooperation: Analyses and Promising paths**

  Panelists: Ryoichi Ishihara, Peter Ramm, Melanie Hentsche, Léo Saint-Martin, EC Representative

  Moderator: Elisabeth Steimetz, VDI-VDE / EPOSS

12:20 – 13:45 Lunch

Afternoon Session

13:45 Advanced Computing main technologies, identified Countries/Institutions for Cooperation, Impact for EU

- **13:45 – 14:50 New Device Architectures, Beyond Von Neumann Computing Architectures & Heterogeneous Integration Nadine Collaert – imec**

14:50– 15:20 Coffee-break

15:20 Advanced Functionalities main technologies, identified Countries/Institutions for Cooperation, Impact for EU

- **15:20-15:55 Smart Sensing: Han Shao –Tyndall**
- **15:55-16:30 Power Devices: Markus Pfeffer: Fraunhofer**
- **16:30-17:05 Energy Harvesting: Gustavo Ardila – Grenoble INP**
- **17:05-17:40 Photonics: Wim Bogaerts - UGent**

17:40 Panel Future strategy for International Cooperation: International & Industrial vision

Panelists: Fabrice Graignic – ST, Patrick Gogez – AENEAS, Sophie Cordeiro – Bosch, Laith Altimine- SEMI Europe, Paolo Gargini – IRDS, Werner Steinhoegl – Head of Sector, European Commission DG CNECT

Moderator: Irina Ionica - Grenoble INP

18:40 Closing remarks – Francis Balestra – ICOS Coordinator

18:50– 20:30 Networking Cocktail

Detailed Programme

Welcome Address

Speaker

Pierre Chastanet is Head of the Unit for Microelectronics and Photonics at the European Commission, where he manages the development of European semiconductor policy and the implementation of the European Chips Act and the preparation of a Chips Act 2.0.
Mr. Chastanet has been working for 20 years in the European Commission, supervising different digital policies in the areas of cloud, data flows, software, cybersecurity, privacy, green ICT, and telecom innovation.
Prior to that, Mr. Chastanet gained over 8 years of business experience, mostly in various IT management positions in a large multinational company.
He holds a M.Sc. in Telecommunication Engineering from Telecom Paris (Polytechnic Institute of Paris), a M.A. in International Politics from the Free University of Brussels (ULB), and a B.Sc. in Economics from the London School of Economics and Political Science. He also earned a Leadership Executive Certificate from the Harvard Kennedy School of Government.

Title: Introduction

Abstract

This presentation will introduce and summarize key results of the Horizon Europe ICOS CSA project dedicated to International Cooperation On Semiconductors. International cooperation is key for knowledge exchange, speeding up technological innovation, reducing cost by avoiding duplicated research, boosting the resilience of the semiconductor value chains, manage risks due to the turbulent geopolitical context, overcoming skill shortage, overcome gaps in standardisation, strengthen Europe's position in the semiconductor area representing pivotal technologies for almost all existing industrial sectors, is encouraged by the new strategies of leading semiconductor countries and is one of the objectives of the EU Chips Act. The recent ICOS results will be highlighted in this Workshop.

Speaker:

Francis Balestra, CNRS Research Director at CROMA, is Director Emeritus of the European SINANO Institute and President of IEEE Electron Device Society France, and has been Director of several Research labs. He coordinated several European Projects (NEREID, NANOFUNCTION, NANOSIL, etc.) that have represented unprecedented collaborations in Europe in the field of Nanoelectronics, and is currently coordinator of the Horizon Europe ICOS project dedicated to International Cooperation on Semiconductors with leading semiconductor countries.
He founded and organized many international Conferences, and has co-authored more than 500 publications.
He is member of several European Scientific Councils, of the Advisory Committees of International Journals and of the IRDS (International Roadmap for Devices and Systems) International Roadmap Committee, as representative of Europe.

Economic Analysis of Semiconductor Value Chain

Title: Analysis of the International Economic Landscape to Identify Cooperation Opportunities

Abstract

Léo will present the current positioning of the European Union's industrial ecosystem within the global semiconductor value chain, particularly in comparison with the United States, China, Japan, South Korea, Taiwan, and Singapore. Leveraging three years of market research and discussion along the ICOS consortium, he will highlight potential areas for cooperation amid increasing geopolitical and trade tensions.

Speaker

Léo Saint-Martin is a strategy consultant and partner at DECISION Etudes & Conseil. DECISION provides market research and strategic consulting across the electronics value chain to policymakers, industry associations, and private companies at both the EU and global levels.

Since 2023, Léo has been managing the economic analysis of the semiconductor industry within the ICOS project (International Cooperation On Semiconductors), on behalf of DECISION.

Since September 2025, he has led the mission "Measures to support the implementation of the EU Chips Act Pillar 3", which includes strategic mapping and monitoring of the semiconductor value chain, as well as support in case of crisis.

Léo has also overseen the analysis of skills intelligence and talent gaps of the European semiconductor industry since 2019 within the framework of the European Chips Skills Academy project (formerly METIS - Microelectronics Training, Industry and Skills).

He has managed and authored numerous studies carried out by DECISION for the European Commission, French government bodies, industry associations, and companies on topics related to the electronics value chain and end-user industries (automotive, defence & security, industrial & robotics, cloud computing, etc.).

Before joining DECISION Etudes & Conseil, Léo conducted several theoretical and empirical research projects in economics and statistics, focusing on local and international development issues.

He holds a master's degree in Economics from Paris-Dauphine University, a bachelor's degree in Economics and Law from Paris Ouest Nanterre-La Défense University, and an advanced certificate in Corporate Finance from HEC Paris.

Foundation of International Cooperation: Analyses and Promising Path

Title: From the technology landscape to cooperation: How to apply filters for priorities

Abstract

So far ICOS has analyzed economic landscape in the semiconductor value chains, scanned current and future semiconductor technologies in the world, and have identified needs and challenges in EU. Based on those, a long list of potential cases of international cooperations between EU and like-minded countries in semiconductor has been identified. In this presentation, we will present how to prioritize these concrete cases and narrow down to a short list by applying filters based on societal, environmental, economic, scientific and policy-driven considerations. We will present a preliminary result of concrete recommendation of international cooperation in semiconductor value chains between EU and other countries that address the critical challenges and needs.

Speaker

Ryoichi Ishihara is an associate professor in Electrical Engineering, Mathematics and Computer Science (EEMCS) at TUDelft and also group leader in QuTech. He is also co-founder and scientific advisor of startup company Mizusense B.V..

He received his PhD from Tokyo Institute of Technology in 1996. In the same year he moved to TU Delft, the faculty of EEMCS, the department of microelectronics, and did research on thin-film electronics and monolithic 3D integration of transistors. From 2015 he is at the department of quantum and computer engineering and QuTech. His current research focus is on an unconventional computing and sensors with on-chip heterogeneous integration of qubit in diamond, electronic and photonic circuits.

Title: Feedback loop: Validation of ICOS recommendation with the community

Abstract

The presentation will provide a bottom-up view from Europe's semiconductor community on how international cooperation can best support Europe's position in the global value chain. We collected feedback from the community through the combination of targeted expert interviews, surveys and panel discussions at major events which in sum produced a rich, coherent picture. Taken together, the feedback from large industry, SMEs and RTOs/Academics converges central messages for the European Commission and the wider ICOS agenda. The USA, Japan, South Korea and Taiwan are consistently identified as the most attractive partners for joint research and development (R&D). They are valued for their excellence and complementary strengths in advanced CMOS, packaging, memory, materials, system integration, and

AI/datacom. Canada and Singapore emerge as lower risk but attractive partners in photonics, packaging and quantum/future compute, while India is seen as a promising source of design talent and as a future market, especially in combination with its growing semiconductor education and policy initiatives. International R&D cooperation should be treated as a central instrument to implement a focused sovereignty strategy. Across all categories, there is strong support for staff and student exchanges as a high-impact tool for building trust, understanding and long-term capability.

Speaker

Dr. Peter Ramm is head of Strategic Projects at Fraunhofer EMFT Munich, responsible for initiation and steering of strategic projects and international research co-operations. He received Masters and PhD degrees from the University of Regensburg and subsequently worked for Siemens in the DRAM facility where he was responsible for the overall process integration. In 1988, he joined Fraunhofer, focusing for more than 35 years on 3D integration technologies. Ramm developed and patented 3DIC approaches with particular focus on heterogenous integration. Peter Ramm is author of over 130 publications and 30 patent families.
He received the "Ashman Award 2009" from IMAPS and the 2020 IEEE Technical Field Award "For Pioneering Contributions Leading to the Commercialization of 3D Wafer and Die level Stacking Packaging".
Peter Ramm is Senior Member IEEE, IMAPS Fellow and Life Member.

Title: International cooperation – which paths are most promising?

Abstract

This presentation explores how successful international cooperation in the semiconductor industry can be achieved. It outlines potential opportunities in relation to strategic priorities, supply chain resilience, and collaborative research and development. The goal is to strengthen global partnerships and, in turn, reinforce Europe's semiconductor ecosystem. The proposed forward-looking pathways take into account geopolitical considerations and emerging technologies.

Speaker

Dr. Melanie Hentsche is a highly experienced materials scientist and project manager specializing in electronics and microsystems. She holds a PhD in Materials Science and has a strong track record in research and development, including roles at different Fraunhofer institutes and SolarWorld Innovations GmbH. She has successfully led numerous publicly funded research projects and demonstrates strong communication and coordination skills. Since 2022, she has been a consultant and project manager at VDE/VDI IT. Her expertise encompasses renewable energies, photovoltaics, project management, material development, and characterization.

Panel Foundation of International Cooperation: Analyses, Priorities, Feedback and Promising paths

Abstract Panel Discussion:

The planned panel discussion will present the key findings of the ICOS Project including the final recommendations and their path. Three concise short presentations will first explain where the consortium see the priorities of cooperation, followed by feedback from the semiconductor community and finally the recommendations derived from this. Building on this, the partners from ICOS will discuss the most important recommendations in the panel and place them in an international context. Furthermore, their significance for future cooperation and strategic decision-making processes will be highlighted.

Panelists: Panelists: Ryoichi Ishihara, Peter Ramm, Melanie Hentsche, Léo Saint-Martin, EC Representative

Moderator: Elisabeth Steimetz, VDI-VDE / EPOSS

Elisabeth Steimetz studied physics at RWTH Aachen, Germany, with a focus on semiconductor technologies and earned her Ph.D. from Technical University Berlin in in-situ metrology of III-V materials. She has more than 20 years of experience in compound semiconductors, particularly in LED and laser fabrication and characterization. As co-founder of LayTec GmbH, she helped build the company and served for 13 years as Director of Strategic Marketing & Sales.

In 2013, Elisabeth joined VDI/VDE Innovation and Technology GmbH as a project officer for national and European funded projects in Electronic Components and Systems. Since 2019, she has been Director of EPoSS – the European Association on Smart Systems Integration – operated by VDI/VDE IT. She is lead EPoSS delegate to the Governing Board of the Chips JU.

Elisabeth chaired the European Strategic Research Agenda for Electronic Components and Systems. She is also a member of the ICOS Advisory Board.

Advanced Computing main technologies, identified Countries/Institutions for Cooperation, Impact for EU

Title: New Device Architectures, Beyond Von Neumann Computing Architectures & Heterogeneous Integration

Abstract

AI-driven compute explosion is reshaping semiconductor priorities, pushing device technologies to extend CMOS beyond its traditional limits. Nanosheet transistors, CFETs, and the broader CMOS 2.0 vision enable continued density scaling and improved power delivery, forming the foundation for next-generation logic. As monolithic approaches stall, heterogeneous integration and chiplet-based design become essential to sustain performance and energy efficiency. At the same time, escalating power densities make thermal and energy management decisive, requiring advanced cooling and energy-aware design from the outset. In this landscape, system-level scaling, spanning devices to packaged systems, emerges as a critical enabler for future compute platforms. At the same time, in-memory computing, neuromorphic processors, and early quantum platforms introduce fundamentally new compute models tailored for data-centric and energy-efficient operation.

These advances form the core of next-generation Advanced Computing technologies and align with efforts across leading institutions around the globe. Collaboration opportunities span leading innovation hubs, including major research institutes and industry players across the world, whose expertise in heterogeneous integration, memory-centric architectures, quantum platforms, and advanced packaging can significantly accelerate Europe's technology development.

For the EU, engaging in these partnerships strengthens industrial competitiveness, opens pathways to emerging markets in energy-efficient AI/HPC hardware, and drives the creation of disruptive knowledge essential for securing long-term technological sovereignty.

Speaker

Nadine Collaert is an imec fellow and part-time professor at VUB (Brussels, Belgium). She is currently leading the technology office in the connected computing sector, focusing on identifying cutting-edge advancements in wireless, wireline, optical communications, and emerging computing technologies to drive future innovation. Before that, she was responsible for the advanced RF program looking at heterogeneous integration of III-V/III-N devices with advanced CMOS to tackle the challenges of next generation mobile communication. Before that, she was program director of the LOGIC Beyond Si program focused on the research on novel CMOS devices and new material-enabled device and system approaches to increase functionality.

She has been involved in the theory, design, and technology of FinFET devices, emerging memories, transducers for biomedical applications and the integration and characterization of biocompatible materials. She has a PhD in electrical engineering from KU Leuven and she holds more than 500 publications and more than 15 patents in the field of device design and process technology.

Advanced Functionalities main technologies, identified Countries/Institutions for Cooperation, Impact for EU

Title: Smart Sensing

Abstract

Silicon-based electrical and optical sensors are key enabling technologies for Europe's semiconductor ecosystem, supporting strategic applications in computing, photonics, mobility, healthcare, energy, and environmental monitoring. This talk provides a global overview of sensor technologies, integrating market and value-chain perspectives across Europe, Asia, and North America. It highlights advances in CMOS-compatible sensing, integrated photonics, wide-bandgap materials, and heterogeneous integration, aligned with Horizon Europe and EU Chips Act objectives. Opportunities for international cooperation and pilot-line access will be discussed, outlining pathways to strengthen Europe's semiconductor value chain, technological sovereignty, and deployment of smart, point-of-care sensing solutions.

Speaker

Dr Han Shao is a Researcher in the Precision Electrochemical Nanosensor Group at Tyndall National Institute, specialising in the development of electrochemical sensors and integrated microsystems for environmental and agricultural applications. With a strong interdisciplinary background in materials science, electrochemistry, and microfabrication, she focuses on real-time, in-situ monitoring of soil nutrients, water quality, and pollutants across terrestrial and aquatic environments.
Her research aims to address major challenges in climate resilience and sustainability by advancing compact, low-power, and high-sensitivity sensing platforms.
Dr Shao is also exploring next-generation solutions for environmental remediation, including nature-inspired electro-bioreactors for treating agricultural runoff and removing contaminants such as nitrates and heavy metals in the field.
Dr Shao collaborates closely with academic, industrial, and public-sector partners, and is committed to securing major national and EU-level research funding. Her work supports Tyndall's strategic goals in smart sensing, sustainability, and global impact.

Title: Power Devices

Abstract

Power devices based on wide-bandgap and ultra-wide-bandgap semiconductors such as SiC, GaN, Ga_2O_3, and AlN are revolutionizing energy conversion. Lower costs and new architectures enable higher voltages, greater efficiency, and more compact systems in mobility, industry, and data centers. This presentation highlights current trends, challenges, and opportunities of these key technologies.In addition, the presentation addresses the role of international cooperation for Europe, including collaborations with partners outside the European Union. The impact of such cooperation on advancing emerging semiconductor technologies, strengthening industrial competitiveness, enabling access to global markets, and generating new knowledge for disruptive technology development is discussed. The contribution of international partnerships to Europe's position in future power electronics ecosystems is outlined.

Speaker

Dr. Markus Pfeffer holds a degree in Electrical Engineering and a PhD (Dr.-Ing.) specializing in manufacturing optimization from the University of Erlangen-Nuremberg. Since 2002, Markus Pfeffer has been with Fraunhofer IISB in the Semiconductor Technology Business Department, where Markus Pfeffer manages the P-Fab (SiC Processing and Prototype Fabrication). Markus Pfeffer is responsible for quality assurance, process control, and the initiation and supervision of research projects. Markus Pfeffer has contributed to various national and international R&D collaborations in both leadership and supporting roles.

Title: Energy Harvesting

Abstract

Energy harvesting (EH) technologies provides energetic autonomy to small electronic devices and are a key factor in booming technologies like sensors networks and IoT, in particular in applications with specific requirements, where a simple battery would not be sufficient. This presentation will recall the main EH technologies (light EH, mechanical EH, thermal EH, Radio Frequency) including micropower management circuits and energy storage (micro-batteries). The presentation is base in the recent IEEE-IRDS roadmap for More than Moore technologies and will include the identification of the main actors in the field from EU and the world and the impact for Europe of international cooperation.

Speaker

Gustavo Ardila received his BS in electronic engineering and physics from the University of Andes, Colombia in 2002 and 2003, respectively, and his MS in microelectronic and microsystems circuit conception from the National Institute of Applied Science (INSA) in Toulouse, France in 2004. He received his PhD degree in Electrical Engineering in 2008 from the Paul Sabatier University in Toulouse. After 1 year of postdoctoral position in LAAS-CNRS laboratory in Toulouse, in 2009 he became Associate Professor at the Grenoble Alpes University and a researcher at CROMA, Grenoble, France. He is or was involved in several French and European projects related to energy harvesting or sensing applications using piezoelectric materials.
He presently leads the European team to develop the Roadmap of Energy Harvesting technologies contributing to the IEEE IRDS (International Roadmap for Devices and Systems). He is member of the editorial board of the “Energy” section of ISTE-Wiley publisher.

Title: Photonics

Abstract

Photonics is a crucial technology for the growing needs of communication bandwidth over both small and large distances, powering telecom networks and datacenters. Outside of this main application space, photonics is also leveraged for use in quantum technologies and sensing. We will outline recent advances and the technologies driving commercialization of photonic chip technology. A comparative analysis of regional strengths will identify key countries and institutions for strategic cooperation. Based on this, we propose collaboration models to position European photonics at the forefront of innovation, reinforcing competitiveness and resilience.

Speaker

Wim Bogaerts is full professor in the Photonics Research Group of Ghent University and IMEC. He specializes in silicon photonics, design of complex photonics circuits, and programmable photonics. During and after his PhD, he laid the foundations for IMEC's silicon photonics platform, and the multi-project-wafer service ePIXfab which made this technology accessible for many researchers in Europe and beyond. To enable design of these circuits, Wim and his colleagues developed the parametric design software IPKISS. In 2014, Wim co-founded the spin-off company Luceda Photonics, to commercialize IPKISS, which is now used by thousands of designers worldwide.
In 2016, Wim received a consolidator grant from the European Research Council, and returned full-time to Ghent University, with a research focus on programmable photonics.. In 2019 he was an invited professor at EPFL in Lausanne, Switzerland. Since 2023 Wim is also chair of ePIXfab, the European alliance to promote the silicon photonics ecosystem.

PANEL SESSION

Future Strategy for International Cooperation: International & Industrial vision

Moderator

Irina Ionica
Grenoble INP

Paolo Gargini
IRDS

Patrick Cogez
AENEAS

Laith Altimime
SEMI Europe

Sophie Cordeiro
Bosch Gmbh

Fabrice Graignic
ST Microelectronics

Werner Steinhoegl
European Commission
DG CNECT

Panel Future strategy for International Cooperation: International & Industrial vision

Abstract Panel Discussion:

The "Panel Future strategy for International Cooperation: International & Industrial vision" will address perspectives for the international cooperation on semiconductors within the strategic context of Chips Act 2. The discussions can cover various points such as emerging needs, technologies, countries, institutions, value chain & applications, novel potential markets for European industry, challenges related to the recent and potential changes in the geopolitical landscape.

Panelists:

With over 35 years in the semiconductor and electronics sectors, **Fabrice Graignic** has a strong background in strategic initiatives for the European semiconductor industry. He currently works in Public Affairs at STMicroelectronics, focusing on industry associations (AENEAS, SYSTEMATIC) at both European and French levels, representing industry and research across R&I frameworks, including CHIPS-JU. His work emphasizes skills development and international cooperation in the semiconductor field, particularly through the European Chips Act.

Previously, Fabrice played a central role in launching major R&D programs, including the first IPCEI on Microelectronics and the Nano 20XX Programs, and managed production control at several manufacturing plants. He holds an engineering degree from Institut Mines -Télécom and two Bachelor-level Diplomas of Higher Education in Management of Companies and Administrations, and in Electrical Engineering and Industrial IT. He also has strong expertise in EU State Aid notification processes and strategic initiatives within the ECS community.

Sophie Cordeiro is a Project Manager at Robert Bosch GmbH, focusing on publicly funded semiconductor projects. She brings extensive experience from both the perspective of funding recipients and funding authorities.
Her background includes shaping strategic research projects, fostering collaborations, and assessing innovation potential. Previously, as a consultant at VDI/VDE Innovation + Technik GmbH, she managed funding instruments (including IPCEI ME and ME/CT) for German federal ministries in the area of microelectronics.
With a PhD in Nanoelectronics & Nanotechnologies, Sophie combines a solid scientific understanding with practical experience in project and strategy management within technology sectors.

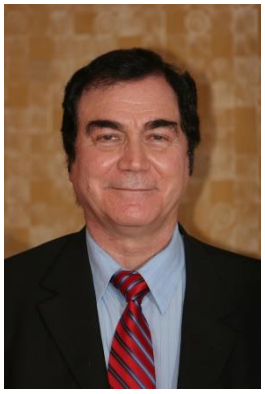
As President of SEMI Europe, **Laith Altimime** leads SEMI's activities in Europe and Middle East and Africa (EMEA). Altimime has P&L responsibility as well as ownership of all Europe region programs and events, including SEMICON Europa. He is responsible for establishing industry Standards, advocacy, community development, expositions, and programs. He provides support and services to SEMI members worldwide that have supply chain interests in Europe. He manages and nurtures

relationships with SEMI members in the region and globally as well as with local association and constituents in industry, government, and academia.

Altimime has more than more than 35 years of international experience in the semiconductor industry. Prior to joining SEMI in 2015, He held senior leadership positions at NEC, KLA-Tencor, Infineon, Qimonda and imec. Altimime holds an MSc from Heriot-Watt University, Scotland.

Dr. Patrick Cogez has been since 2016 with AENEAS, a not-for-profit Industrial Association fostering Research and Innovation and creating an effective funding landscape for its members in the Electronic Components and Systems (ECS) value chain. He is AENEAS Technical Director, current co-Chair of the ECS Strategic Research and Innovation Agenda (having been Chair in 2017-2018 and 2023-2024), and is active in several European collaborative projects on international cooperation and skills development for the microelectronics industry, in particular initiating the ECS summer school. After an early career start in the French Ministry of Industry, Dr. Patrick Cogez spent most of his career in the semiconductors industry. He joined STMicroelectronics in 1988, where he designed, implemented and managed the Information Systems of the Crolles R&D and Manufacturing site. He was later appointed Director for Innovation and External Research.

Dr. Cogez graduated from Ecole Polytechnique and Ecole des Ponts et Chaussées in France, and holds a M.Sc. degree in Operations Research and a PhD in Industrial Engineering, both from the University of California at Berkeley. He completed an Executive MBA programme at NEOMA Business School, Paris. He co-managed three PhD theses and co-authored several articles on the management of disruptive innovation.



In the 70s **Dr. Gargini** was a researcher at Stanford University and at Fairchild Camera and Instrument. In **1980** as manager of MPU technology at Intel transferred into manufacturing the iconic 80286 and 80386. In **1996,** he became Director of Technology Strategy and responsible for worldwide consortia research until **2012.** He was a member of Sematech, SRC, EUV LLC, EIDEC, ASET, IMEC and SIA Boards, and Chairman of the I300I and NRI. From **1998** to **2015**, Dr. Gargini was Chairman of the ITRS sponsored by the WSC. Since **2016** he is the Chairman of the IRDS sponsored by IEEE.

Dr. Werner Steinhögl is Head of Sector in the Microelectronics and Photonics Industry unit of the European Commission in Brussels. He has been serving in the Communications Networks, Content and Technology (CONNECT) directorate general since 2005 in different positions. He has managed R&I support programs spanning from emerging quantum computing technologies over nano- technologies to safety-critical computing and cyber-physical systems. Since 2018 he has been in charge of the photonics public private partnership for research and contributed to the EU Chips Act. Since 2022 he deals also with international collaboration in the semiconductor area.

Before joining the European Commission, he had worked as team leader for 6 years in the semiconductor industry at Infineon and Siemens in Munich, held various positions in corporate research and collaborated with R&D teams in the US. He holds a PhD in solid state physics acquired at the Max-Planck Institute for Fluid Dynamics in Göttingen, Germany, in 1998.

Moderator: Irina Ionica - Grenoble INP

Irina Ionica received the PhD in Micro and nano electronics from the National Polytechnic Institute in Grenoble (INPG), France, in 2005. Since 2006, she is Associate Professor at Grenoble Institute of Technology and develops her research activities at CROMA laboratory, in Minatec. Her current research interests include sensing with ISFET-like structures, second harmonic generation for dielectric-semiconductor interfaces characterization, electrical characterization of silicon-on-insulator (SOI) substrates, nano-characterization by scanning probe microscopy. She had 14 PhD students under her supervision (among which, 6 in progress), she is co-author of 40 journal papers and more than 55 conference papers. She was involved in several local, national and European projects (Nanofunction, Compose3, Reminder, ICOS). She was the coordinator of regional and industrial contracts.

She is in charge of the international exchanges for the students in the Physics department of the Engineering school on Physics, Electronics and Materials from Grenoble-INP since 2021 and of the Mentoring program of Grenoble-INP since 2024.

International Cooperation On Semiconductors

This event is associated with INPACE

This is a side event of ECS BROKERAGE EVENT 2026

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