

The scenery viewed from my research building of Hokkaido University.

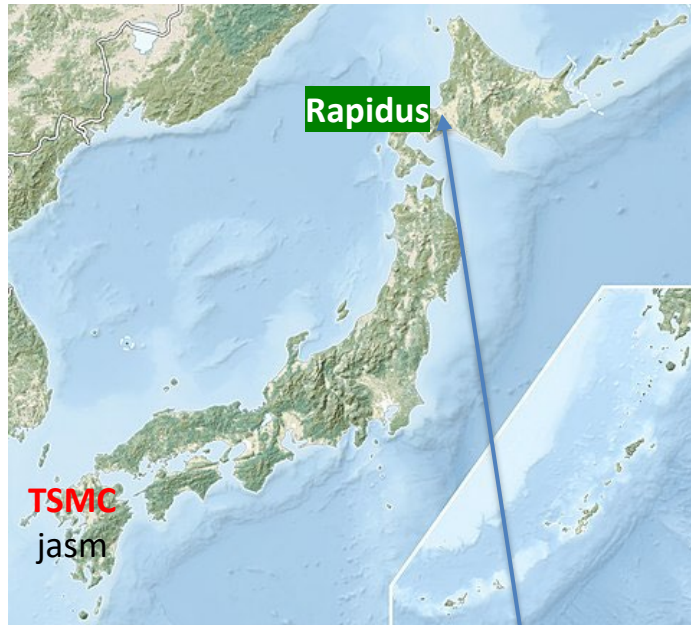


Hiro AKINAGA

Cooperation opportunities, i.e. potential international collaborations on emerging semiconductor technologies

Agenda:

- Short statements of panelists: their organization's (country's) engagement in
 - National Chips Act programs or/and their view on
 - Cooperation opportunities, i.e. potential international collaborations on emerging semiconductor technologies



<https://research.ibm.com/blog/rapidus-ibm-move-closer-to-scaling-out-2-nm-chip-production>

Cooperation opportunities, i.e. potential international collaborations on emerging semiconductor technologies

Ministry of Education, Culture, Sports, Science and Technology
Advanced Research Infrastructure for Materials and Nanotechnology in Japan (ARIM)



Shared use of state-of-the-art facilities and materials data to support R&D
Aiming to develop new materials through the DX revolution

Shared User-facility Network

<https://nanonet.go.jp/>



Institute for Frontier Education and Research on Semiconductors, Hokkaido Univ.



IFERS
Institute for Frontier Education
and Research on Semiconductors

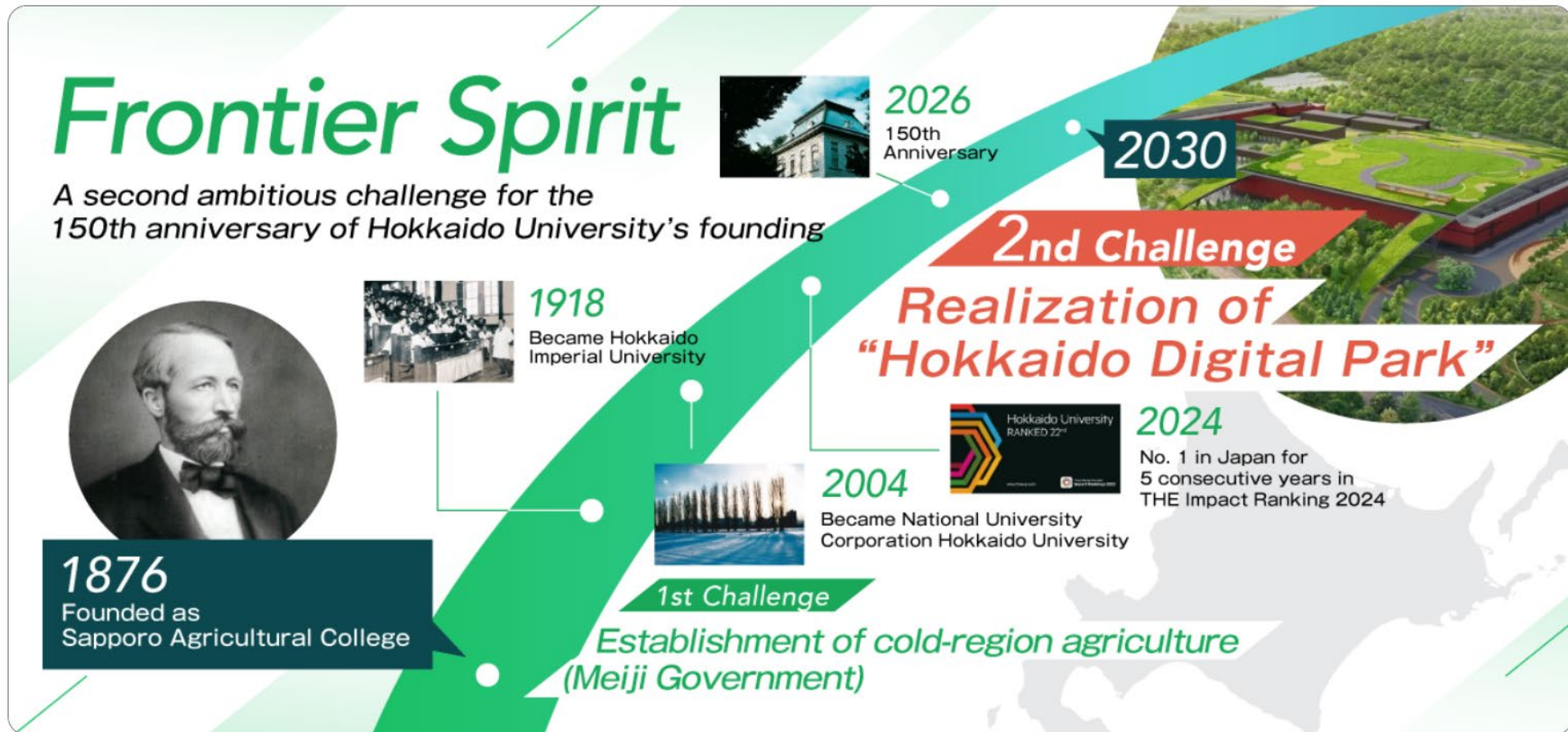
<https://www.semicon.hokudai.ac.jp/en/>

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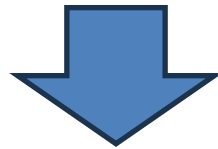
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The preparation of R&D platform to foster emerging research is underway.

Meanwhile, it is urgent to cultivate human resources aimed at implementing these results in society and creating innovation.



Europe has been, and will continue to be, an important destination for young Japanese researchers as an international cooperation partner, including exchange of human resources.

Step 1: Perform a gap analysis with existing standard projects and TC

Step 2: Propose new TC or project of digital twin for semiconductor

Step 3: Promote project proposals by young people in each country through ICOS (INPACE)

<https://www.iso.org/standard/81442.html>

https://www.iec.ch/dyn/www/f?p=103:7:::FSP_ORG_ID:20486