

# Japanese Session Workshop - Sustainable Electronics & International Cooperation on Semiconductors



Location: Grenoble, France

Date: April 26-28th, 2023

Participants: Consortium, International speakers, IRDS  
Roadmaps leaders, EC, EIC & external participants



Japanese Session at the Workshop on “Sustainable Electronics & International Cooperation on Semiconductors” organised in Grenoble, France, on April 26-28

The first ICOS workshop “Sustainable Electronics and International Cooperation On Semiconductors”, a 3 days event organized with the SiNANO Institute, took place on April 26-28th in Grenoble, France. It gathered very high level experts, researchers and industrials of the field, with the objective to share their works and thoughts and open the debate on key issues such as **sustainability in the world of electronics, or the Chips acts**. It also aimed at **facilitating international cooperation and pave the way for future collaborations**.

**Goals:** To have an overview of the strategies of different leading countries in the field of semiconductors, European and International roadmaps and to conduct an initial analysis of the gaps between EU activities and the most promising technologies highlighted in roadmaps.

**Outcomes:** It allowed to discover the strategies of Europe, US and Japan and, following the different roadmaps presentations and discussions afterwards, it strongly helped to gather information and outputs for the initialisation of the deliverables D3.1 & D3.2.

**Impact:** A specific joint Japanese session was organised, giving an overview of the Japanese semiconductor ecosystem as well as discussing scientific and technological trends and international projects. This session, organised in hybrid, gathered experts from Japan from the Ministry of Economy, Trade and Industry, Tokyo University, RENESAS, together with experts from DG-Connect, CEA-Leti, CNRS and Bosch, and supported the implementation of the Digital Partnership with Japan.