



First EU - India Joint Researchers Workshop on Semiconductors



Location: Brussels, Belgium Date: October 9th, 2024

Participants: High level speakers, young researchers, researchers from both side, EC representatives, Chips JU representatives, ICOS members and Indian representatives

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First EU – India Joint Researchers Forum on Semiconductors:

Following the launch of the EU-India Trade and Technology Council in February 2023 and the signing of a Memorandum of Understanding on Semiconductors in November 2023, the first Workshop bringing together both parties to advance cooperation and collaborate on the future of semiconductors took place on October 9th in Brussels, hosted by ICOS.

This event brought together various stakeholders from the ecosystem, including industry leaders, academics, and policymakers from both India and the European Union, to facilitate a comprehensive and constructive exchange on the current state of semiconductors and the potential future developments in the sector through international cooperation.

The presentations and discussions held throughout the day highlighted the complementarity of the Indian and European systems and underscored the need for collaboration to address operational gaps on both side by leveraging the strengths of the other.

The policy session which took place at the beginning of the day emphasized the commitment of both governments to foster a constructive and strong relationship focused on collaboratively developing a semiconductors industry that is capable to address the current challenges.

Mr Sagar Sharma, representative of the India Semiconductor Mission from the Ministry of Electronics and Information Technology, expressed his determination to diversify and strengthen the semiconductor supply chain through investment and cooperation. Mr Pierre Chastanet, Head of the C.3 Microelectronics and Photonics Unit at the European Commission, emphasized the complementarity of the Indian semiconductor landscape with the European one and encouraged collaboration to identify and address vulnerabilities in both systems.

Goals: To evaluate the need of cooperation between EU and India in order to enhance the semiconductors strength from both side. The goal was to evaluate strengths and needs and to connect researchers from the two sides in the framework of the launched of the EU-India Trade and Technology Council and the Memorandum of Understanding on Semiconductors signed between the two.

Outcomes: Presentation of both semiconductors ecosystems and analyse of the potential areas of collaboration between India and the EU. Matchmaking between researchers and attendees from both sides with interactives sessions and networking time.

Impact: Representatives from the Indian Ministry expressed their strong will to invest into the Indian semiconductor ecosystem and to develop cooperation in this area. Pierre Chastanet from the European Commission highlighted the complementarity of India and EU in this domains and encourage working together for useful and efficient collaboration.