

Challenges in Advanced Computing and Functionalities International Cooperation on Semiconductors

Rethinking Education and Training for nowadays semiconductor manufacturing by adapted Bauhaus Education Philosophy

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ICOS WORKSHOP – May 13-14<sup>th</sup> 2024, Athens – EUROSOI-ULIS Conference Name



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- II. STEM\* education Status quo \* Science, Technology, Engineering, and Mathematics
- III. Bauhaus a blueprint from history
- IV. μe-Bauhaus teaching building blocks and current implementation
- V. Outlook









# Motivation



The EU-Chips act and its impact on the economic landscape in Europe, esp. semiconductor industry



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facilities are built

## Framework

Wolfspeed plans multi-billion dollar chip factory in Germany, Handelsblatt reports STMicroelectronics to build integrated Silicon Carbide substrate manufacturing Investment of > 40 bn EUR facility in Italy After a record 2022 fiscal year, Infineon Aa K in EU-chips to increase Oct 5, 2022 Geneva, Switzerland significantly increases its long-term financial targets, and is planning a major investment in a European market share in new factory in Dresden; positive outlook for 2023 GlobalFoundries announces Nov 14, 2022 | Quarterly Report chip production to 20 % mega investment for semiconductor production in New manufacturing Germany, Intel sign deal for chip factory ew facility in Dresden, **TSMC Plans for First German Chip** Fab With Cost Up to €10 Billion TSMC's board could make investment decision in August Project is seeking state funds to build TSMC's first EU plant By Jillian Deutsch and Alberto Nardel 3. Mai 2023 at 14:29 MES

Map of Announced-Planned Builds in Europe for 2023 - 2024. Source: FLMSolutions project tracker

https://www.dpsgroupglobal.com/news-and-insight/european-chips-actand-upcoming-eu-semiconductor-projects-for-2023-2024/

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## **Resulting effects**

- Increased amount of workforce in the relevant specialist areas (shop floor, maintenance, production planning and control)
- Lack of skilled labor
- Lack of training facilities ullet
- Demand for short on-site training periods

ZVEI-Seiter **Requirement level of the main** Fachkräftebedarf in der Halbleiterindustrie occupational groups in the Ziel des EU Chips Act ist es, den Anteil Gun semiconductor industry There is an annual average shortage of almost 82'000 skilled workers relevant for semiconductor industry in 2022/23." - study by ZVEI (Association of the Electronics Industry) 15% 27% There will be a lack of 350'000 employees by the end of the decade in Europe, if the 37% 21% EU market share really doubles. Study by PwC Strategy&, 2023 Helpers w/o specific expertise Vocational trained specialists Specialists with further qualification (incl. B.Sc)

Experts (M.Sc., Phd)

- according to IW specialist database, 2022



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- Increased amount of workforce in the relevant specialist areas (shop floor, maintenance, production planning and control)
- Lack of skilled labor
- Lack of training facilities
- Demand for short on-site training periods

- Secure for the next generation of skilled workers
- Establish further education and training measures that are provided in German as well as in English language
- Create educations contents and measures that are developed (and carried out) with close involvement of the local industry

- Statement of president of Bitcom e.V., 2023







# **STEM education**

## Common status quo in Germany for education in Science, Technology, Engineering, and Mathematics







- Teaching aims at providing knowledge
- No freedom to create



Is this way of teaching sufficient and sustainable enough to overcome the challenges we face?







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How to overcome the challenges?
How to increase attractiveness?
How to excite and to enable engineering students to create innovative solutions?

### Is there a blueprint in history?







## bauhaus – a look back into history











# The bauhaus manifesto

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Unity of craft, technology, art, and industry – a new form of teaching and practical training

#### Self-conception:

The ultimate goal of all artistic activity is construction.



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#### Bauhaus principles and their implementation in education

#### **Principles**

- Art cannot be taught itself, but the craft behind it can be.
- Simultaneously develop the student's creative substance through encouragement on the one hand and their intellect through rigorous studies on the other.



Preparatory course -

the prerequisites creator for further education

- Exploration of student personality and creativity
- Addressing the elementary aspects and requirements of creation without specific purpose:
- > Materials
- Shaping and presentation options
- Constructions and respective methods







#### **Principles**

- Artists and craftsmen must work together to create new total artwork; both skills are required.
- > Form follows function.
- Intelligently use resources.



Workshop training – practical work and training as apprentices and education in accompanying subjects

- Explorative work to gain precise knowledge of materials and work processes by trial and error
- Focus on different materials and tools and how to use them to create designs for standard items for everyday use
- > Synthesis of all arts
- Joint planning and realization of projects by students and masters

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Use of recent and adequate technology



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CONSTRUCTION The students visit workshops, each dedicated to a different material and accompanied by two masters.

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STONE

One master teaches them the practical aspects of craftsmanship, the other one the beauty of form.

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WOOD



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COLOUR



, V., Varbella, S. (2022). *BAUHAUS – die illustrierte Geschichte* (2. Ed.). Prestel M. (2019)/ Bauhaus-Archiv: Bauhaus 1919-1933, Taschen



#### Bauhaus principles and their implementation in education

#### **Principles**

- Focus on simplicity and effectiveness.
- Stay on the pulse of society.
- Foster ongoing progress and development.



Examed and most talented students were admitted to apprenticeship in construction

Enabled to shape the future through education based on three pillars reflected in Bauhaus symbolism





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## How to adapt to excite and to enable engineering students to create innovative solutions?

## The µe-bauhaus teaching













IV.

The building blocks:

Unity of theoretical knowledge in material and technology and skills for semiconductor manufacturing

#### Self-conception:

The ultimate goal of semiconductor engineering and technology is the manufacturing of the semiconductor chip fulfilling the specified electronic functions and operations under the predetermined parameters.



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#### What needs to be understood – the academic teaching part

- Basic knowledge in STEM subjects and fundamental experimental methods
- In-depth study of current semiconductor technologies (bipolar, (C)MOS, photonic, quantum)
- Understanding of electronic, mechanical, thermal and chemical properties of relevant materials (semiconductors, metals, isolators)
- Conceptual knowledge regarding circuits and semiconductor devices.





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### The technical sound education

- Targeted final state: 4 different theoretical lecture series modules which build on each other, available for hybrid teaching, all in German and English language
- Structured, retraceable design and symbolism used
- Curriculum development, learning module design and creation is currently ongoing

Partly developed within the FMD-QNC project, WP 11 on microelectronic academy





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Quantum Technology

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Studies

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**WORKSHOP** 

Complement theoretical education with "hands-on" training in workshops:

- concept development for innovative ideas
- out-of-the box thinking
- jointly finding solutions

#### From the first idea towards a prototype.





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#### The practice-oriented part in everyday application



# The workshop – the cleanroom



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# Integrate learners into everyday R&D and prototype manufacturing

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#### Education & training concept of µe-Bauhaus Erlangen-Nuremberg



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#### Implementation: The PhD candidate line at Fraunhofer IISB





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# µe-bauhaus in future

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#### How to proceed to drive implementation forward?

#### Needs-oriented increase of training capabilities

- Concept development for apprenticeship advancement in semiconductor technology
- Open vocational education towards the academic path
- Combined programs containing academic and vocational education
- Train the teachers
- Retraining programs for professionals
- Exchange and networking with other initiatives



Blueprints Best-practice examples







# What is needed to educate the shapers of the future?





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# **THANKYOU!**



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icos-semiconductors.eu



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