

Challenges in advanced computing and functionalities, International Cooperation

May 13-14, Athens



Glyfada-Athens

icos-semiconductors.eu

Preliminary Agenda

Day 1: May 13 afternoon

14:00 Skills Shortage Session

- 14:00 Introduction to the Workshop, *Francis Balestra, ICOS Coordinator, Grenoble INP/CNRS SiNANO*
- 14:15 The European Chips Skills Academy Project – Assessing the Chips skills gap and defining mechanisms to address it – *Patrick Cogez AENEAS; Léo Saint-Martin DECISION Etudes & Conseil*
- 14:45 Building tomorrow's workforce: early insights from the semiconductor Thematic Working Group on Skills – *Leonardo Freitas, ALLPROS.EU*
- 15:15 IEEE initiative – *Paolo Gargini, IRDS*
- 15:45 Rethinking Education and Training for nowadays semiconductor manufacturing by adapted Bauhaus Education Philosophy – *Fraunhofer IISB*
- 16:15 SINANO Academy – *Giorgos Fagas, Tyndall/SiNANO Institute*

16:45 - *Coffee break*

17:15 First Session of the ICOS results

- Presentation of the final ICOS results on Economic analysis of the EU & international semiconductor ecosystems
Léo St Martin – DECISION Etudes & Conseil

18:45 – Networking Cocktail



Day 2: May 14 morning

8:45 Second Session of the ICOS results

- 8:45 Introduction and Presentation of the results of the ICOS Survey - *Francis Balestra, ICOS coordinator*
- 9:15 Presentation of the preliminary ICOS results on International Technology Highlights on Advanced Functionalities - *Markus Pfeffer, Fraunhofer IISB; Jyrki Kiihamäki, VTT*

10:00 Coffee break

- 10:30 Presentation of the preliminary ICOS results on International technology Highlights on Advanced Computing - *Nadine Collaert, imec*
- 11:15 Addressing Generic Challenges in the Semiconductor Ecosystem through International Cooperation - *Roel Baets, Ugent/ imec*

12:00 Lunch

Day 2: May 14 Afternoon

13:45 Third ICOS/INPACE Joint International Session

- 13:45 Introduction to the ICOS/INPACE International Session, *Francis Balestra, Grenoble INP/CNRS/SiNANO*
- 14:00 The Dawn of the New Electronics Industry (surfing over Chips Acts waves) – *Paolo Gargini, IRDS*
- 14:30 Building and Powering the GaN Ecosystem in India - *Sridhar Srinivasan, GEECI - IISc*
- 15:00 Integrated ultra-low power molecular sensors for health-condition and monitoring - *Ken Uchida, University of Tokyo*

- 15:30 (Silicon) Photonic Integrated Circuit Ecosystem in India: Opportunities and Challenges - *Shankar Kumar Selvaraja, Indian Institute of Science*

16:00 Coffee break

- 16:30 IBM Status and Trends in Advanced Technologies - *James Sexton, IBM Fellow*
- 17:00 Heterogeneous 3D Chiplet Integration – *Mitsumasa Koyanagi, Tohoku University*
- 17:30 Driving Net-Zero with GaN: Advancing Power and RF GaN Technology through Industry-oriented Models for Foundry & Circuit Designers – *Sheikh Aamir Ahsan, National Institute of Technology Srinagar*

18:00 PANEL SESSION “Opportunities for International Cooperation”

Chair: Peter Ramm, Fraunhofer EMFT

With:

Paolo Gargini, IRDS

Shankar Kumar Selvaraja, Indian Institute of Science

Sridhar Srinivasan, IISC

Mitsumasa Koyanagi, Tohoku University

Ken Uchida, Tokyo University

James Sexton, IBM

19:00 – Networking Cocktail