



EU - SOUTH KOREA – Joint Researchers Forum
on Semiconductors



Electronic synapses enabled by epitaxial Hafnia-based ferroelectric field effect memristors on Silicon

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N. Siannas et al., *Adv. Funct. Mater.* (2023); doi: [10.1002/adfm.202311767](https://doi.org/10.1002/adfm.202311767)



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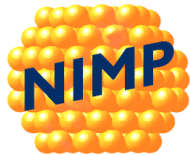
Name



NCSR: N. Siannas, C. Zacharaki, P. Tsipas and A. Dimoulas
Growth, processing, physical and electrical characterization



HZB: D.-J Kim W. Hamouda and C. Dubourdieu
Growth of epi n-STO/Si wafers

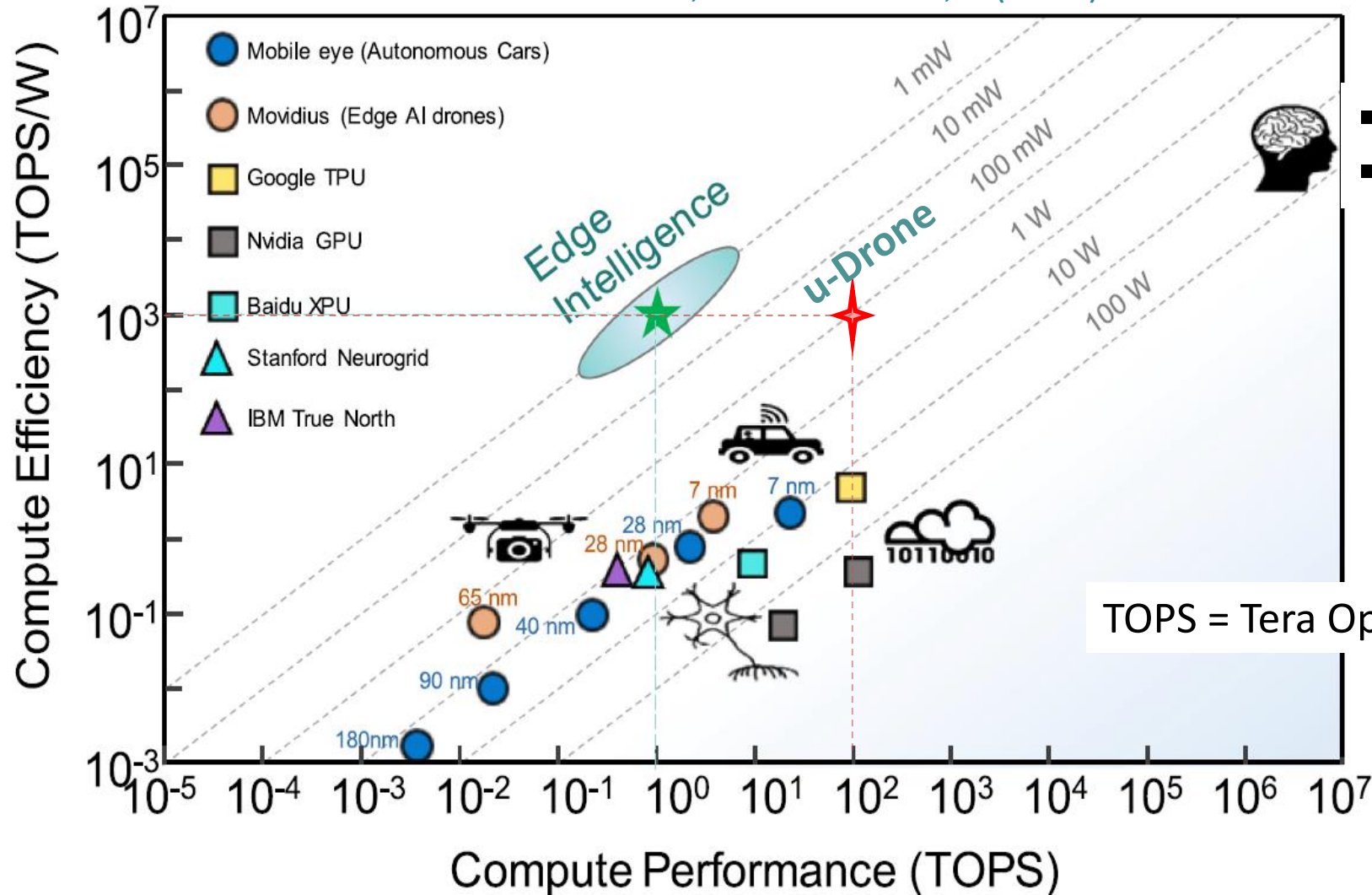


NIMP: C. Istrate and L. Pintilie
HR TEM



IKZ: M. Schmidbauer
X-ray Diffraction

A. Keshavarzi et al., *IEEE Micro*. 40, 1 (2020)

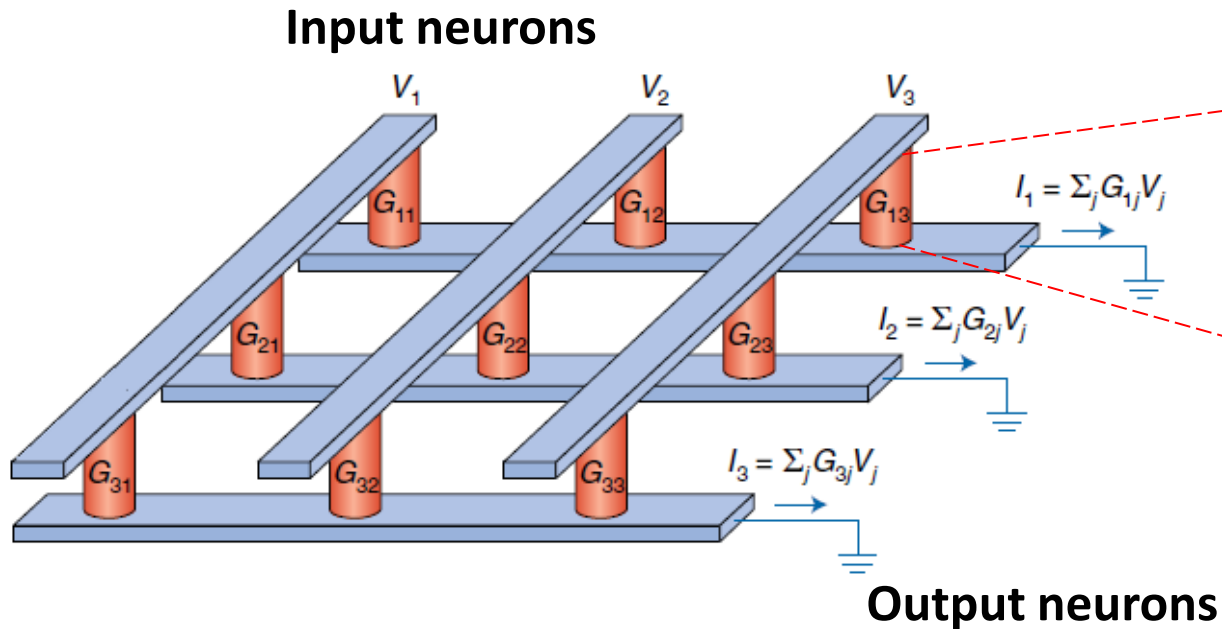


TOPS = Tera Operations Per Second

- Massive parallelism
- in-memory computing

Cross bar arrays-AI accelerators

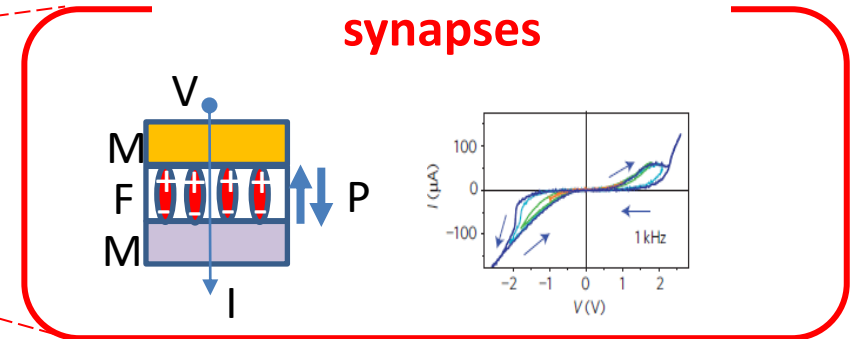
Multiply and Accumulate



A. Chanthbouala et al., *Nat. Mater.* **11**, 860 (2012)
 D. J. Kim et al., *Nano Lett.* **12**, 5697 (2012)

Perovskite (BaTiO_3)

Ferroelectric memristor synapses

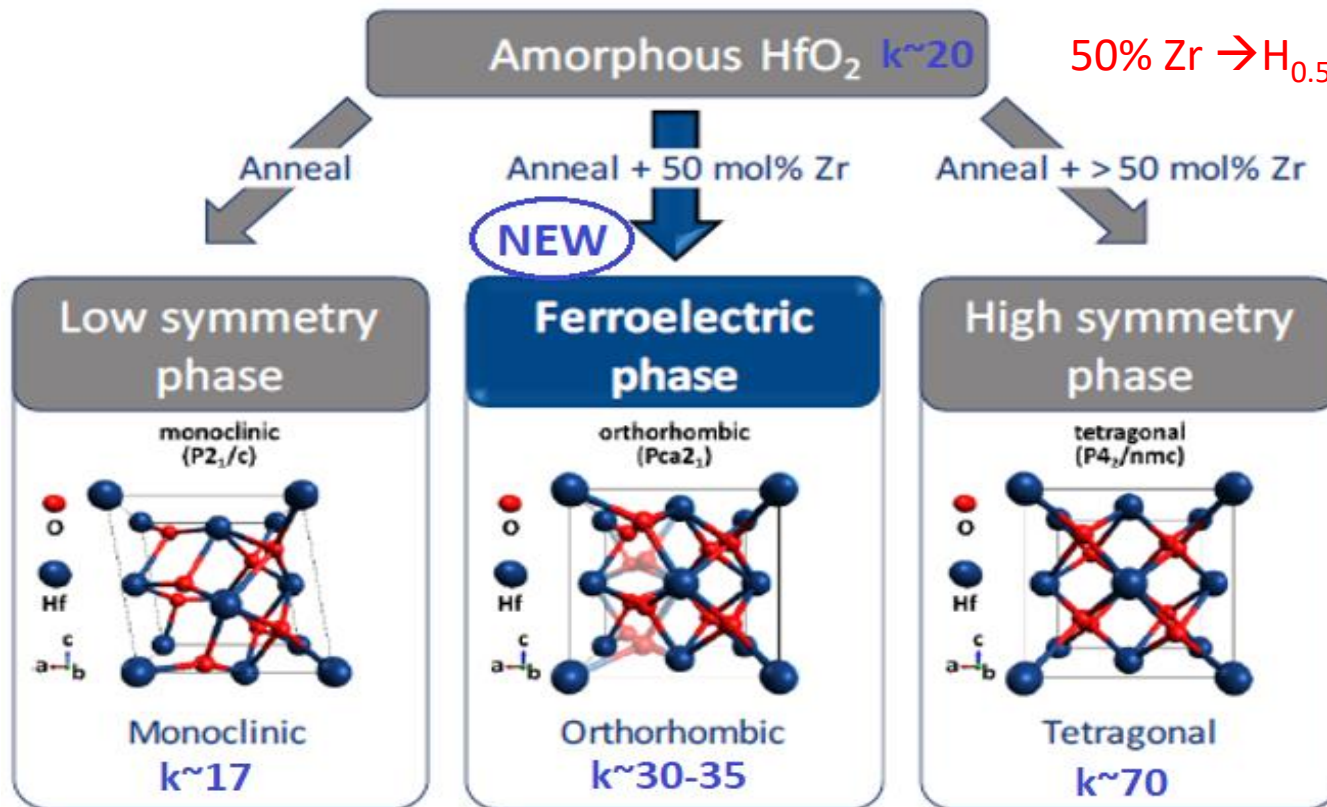


- Update and store conductance weights
- Analog **in-memory** computing
- Voltage driven memristors \rightarrow low power

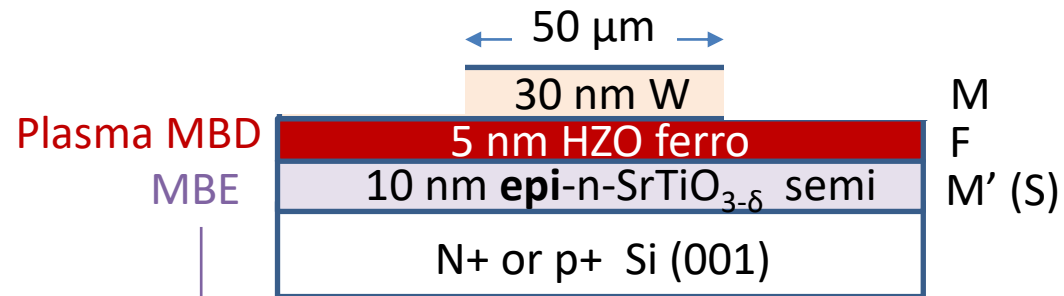
D. Ielmini, H.-S. P. Wong, *Nat. Electron.* **1**, 333 (2018)





Discovered in 2007-first published 2011, Qimonda [T. S. Böscke et al, Appl. Phys. Lett. 99, 102903 \(2011\)](#)

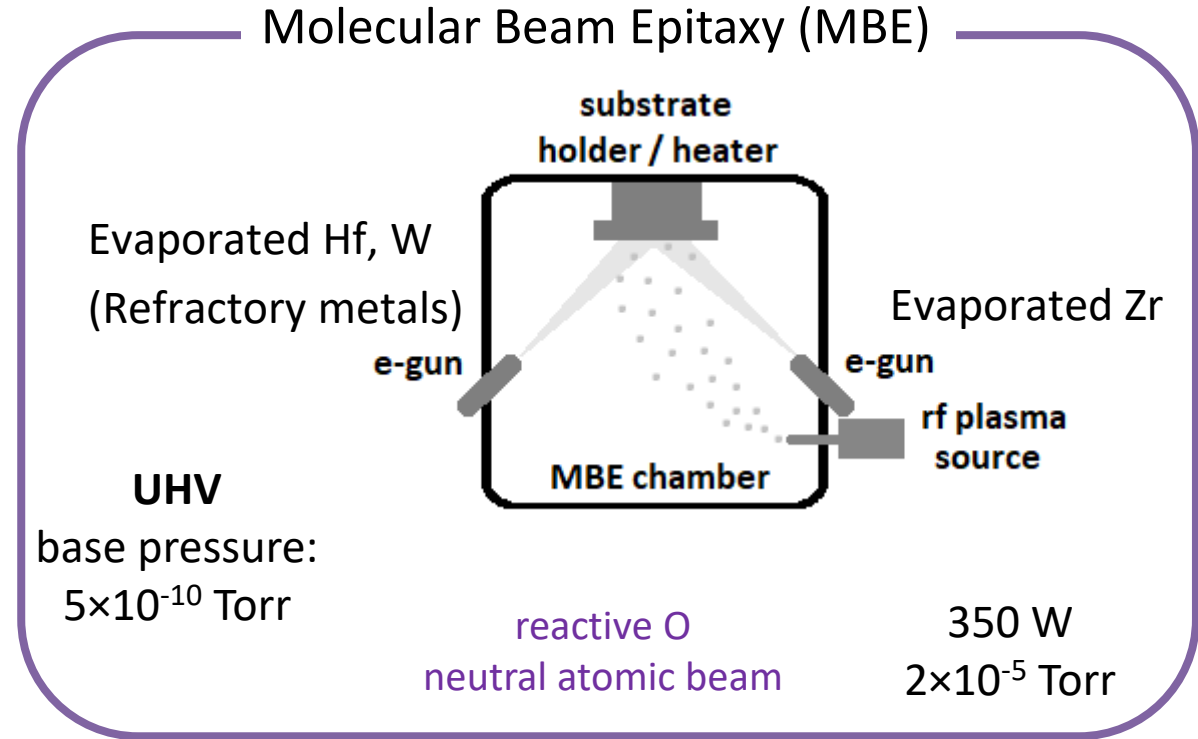
Stabilization of the ferroelectric phase: doping (Si, Zr,..), stress, annealing, film thickness ...



Compatible with Si processing
(HfO₂ used as high-k dielectric/CMOS scaling)



-  Deliberately grown O deficient to act as bottom electrode
O vacancy donors $V_{\text{O}}=0.56\% \rightarrow$ N-type $\rho=0.02 \Omega\text{cm}$
-  $T_{\text{growth}}(\text{HZO})=120^{\circ}\text{C}$, amorphous
-  Crystallization annealing 420°C , 400 s
-  Optical litho + etching or lift-off

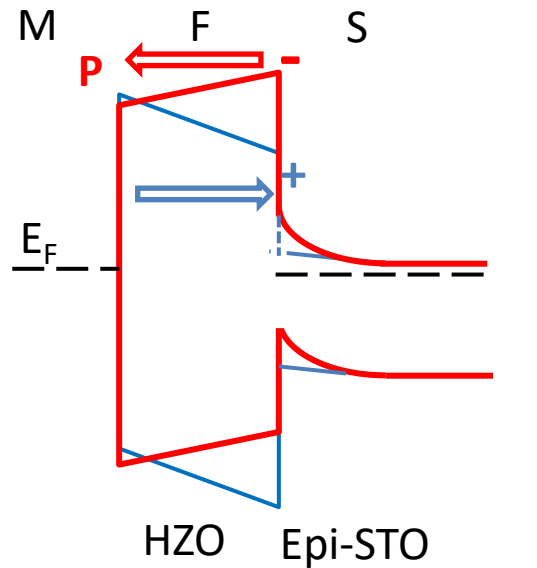


C. Zacharaki et al., *Appl. Phys. Lett.*, **114**, 112901 (2019)

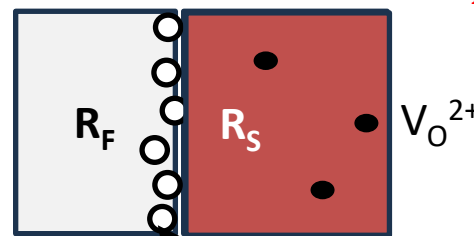
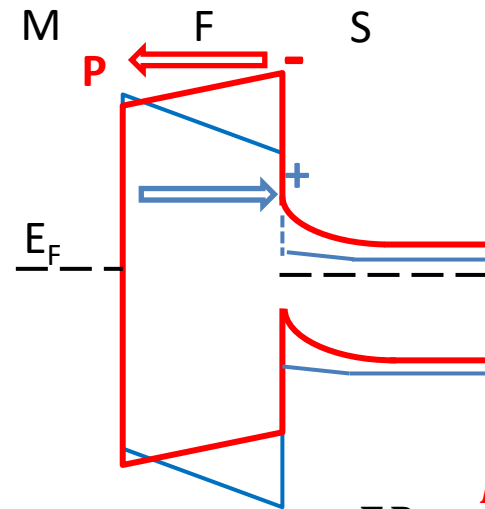
C. Zacharaki et al., *ACS Appl. Electron. Mater.* **4** 2815 (2022)

Ferroelectric programming of the semiconductor resistance

N. Siannas et al., *Adv. Funct. Mater.* (2023); doi: 10.1002/adfm.202311767



Electronic field effect



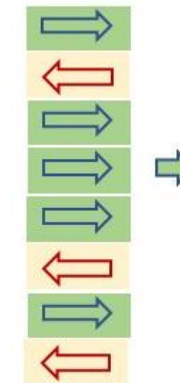
O²⁻ V_O²⁺ Ionic Redox

$$ER = \frac{R_{S,OFF} - R_{S,ON}}{R_{S,OFF}}$$

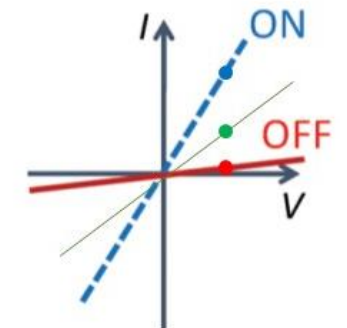
Below 5 nm HZO,
ferroelectricity is unstable

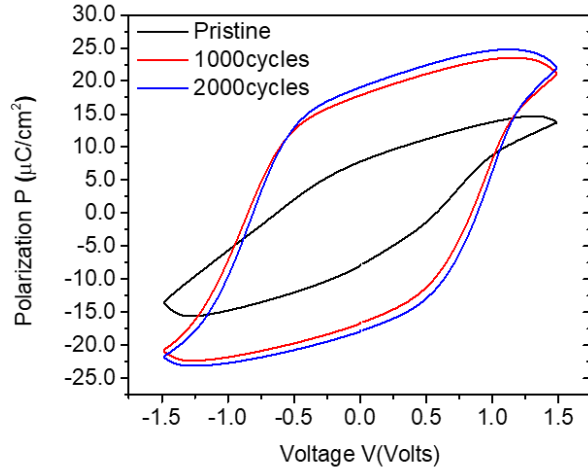
N. Siannas et al.,
Comms. Phys. **5**, 178 (2022)

Partial
Polarization

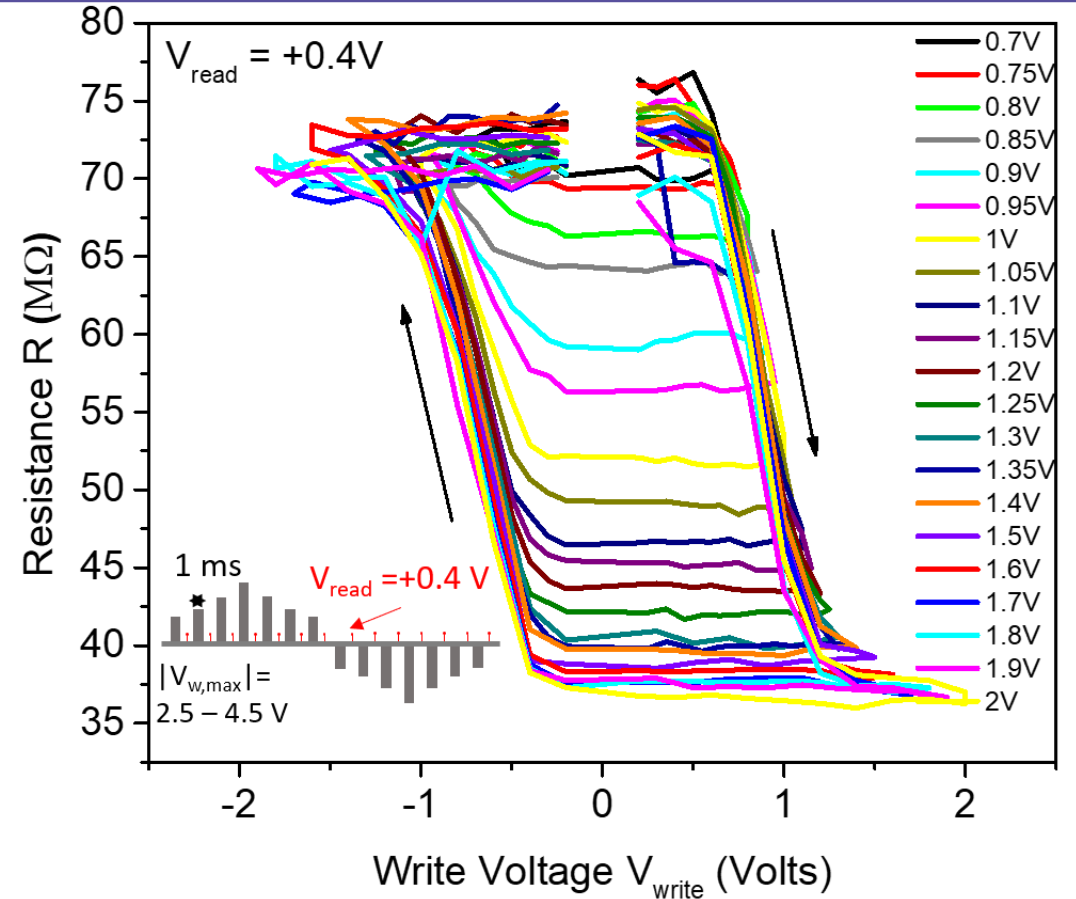
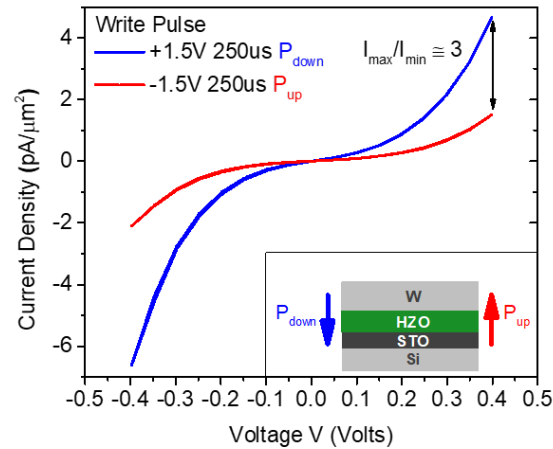
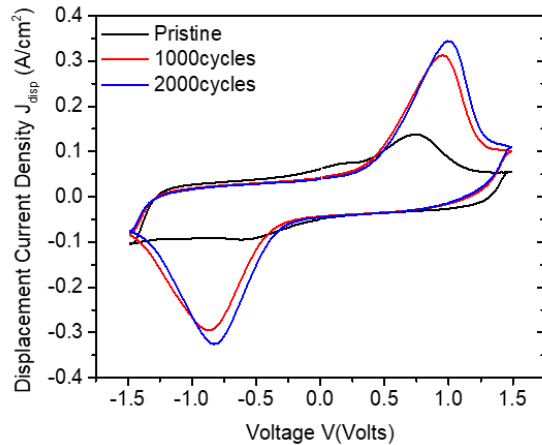


Intermediate NV
resistance states



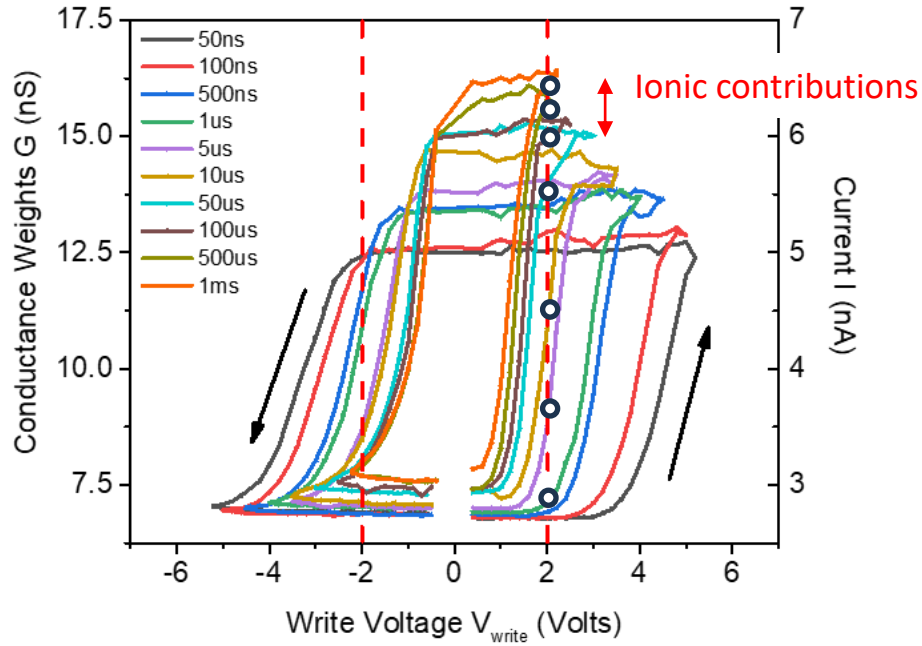


- $P_r \sim 14 \mu\text{C}/\text{cm}^2$
- $V_c < 1\text{V}$
low voltage operation
- Low I_{ON} / low ER

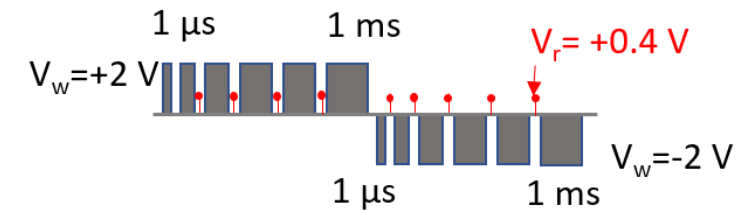
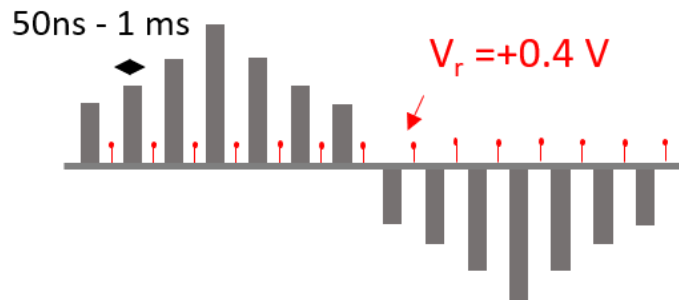
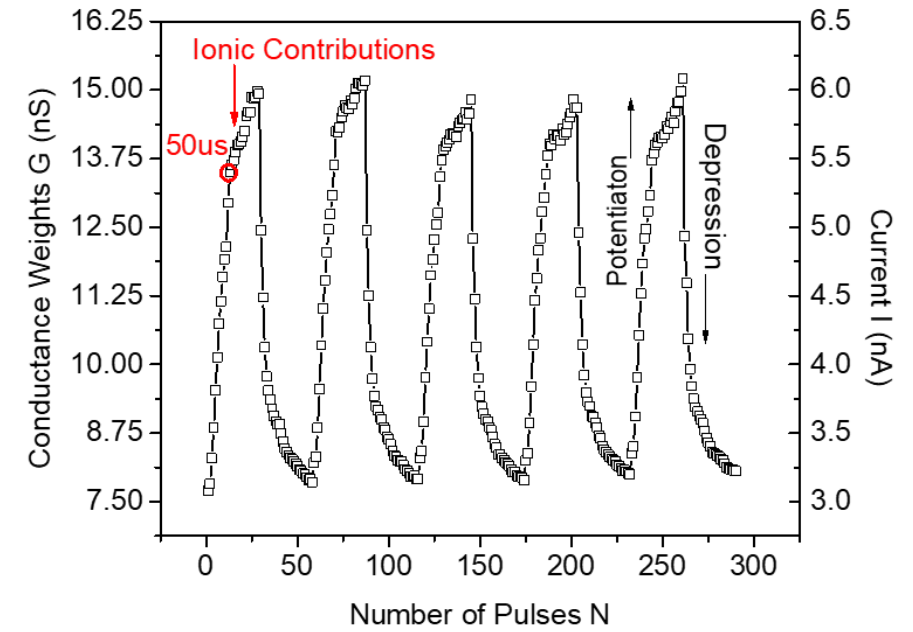


- Narrow memory window $\pm 1\text{V} \rightarrow$
compatible with ferroelectric switching ($V_c = 1\text{V}$)
- Many stable intermediate resistance states (16, 4bit)

Time-voltage trade-off



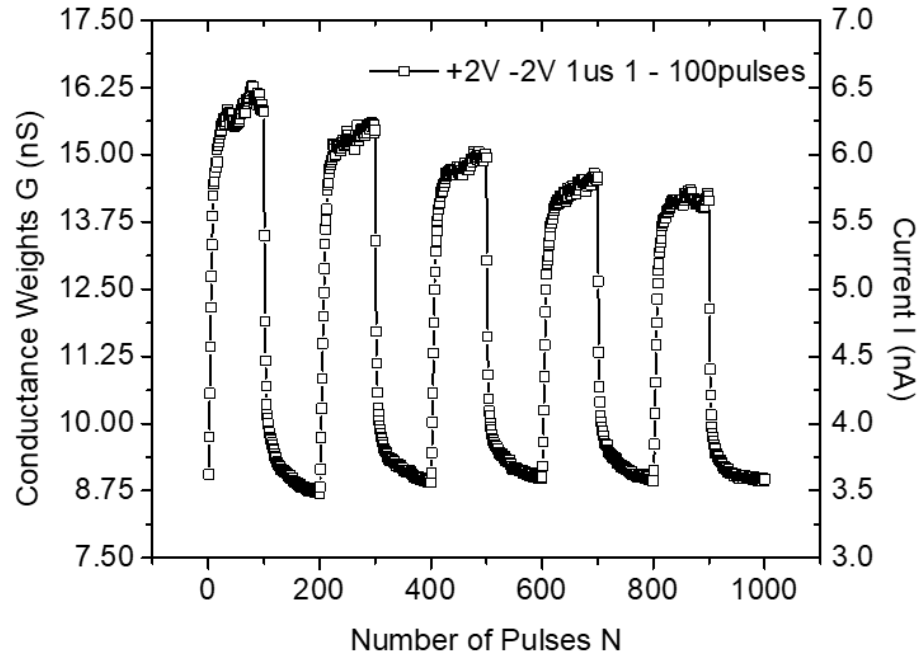
Variable width pulses



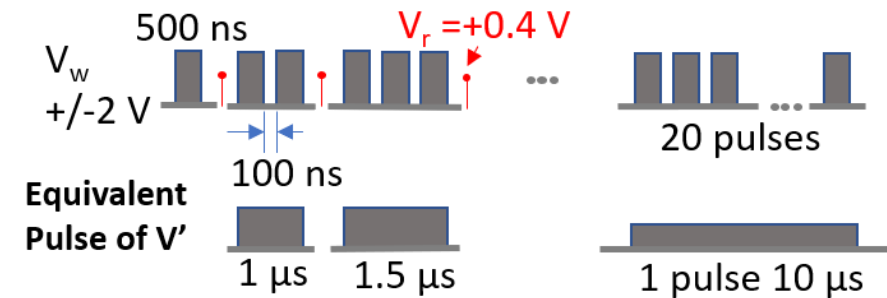
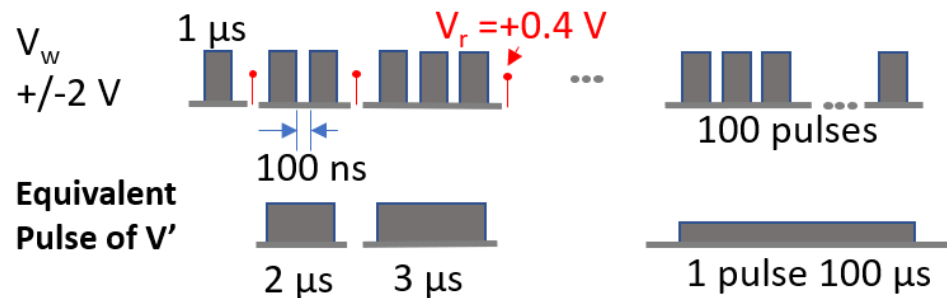
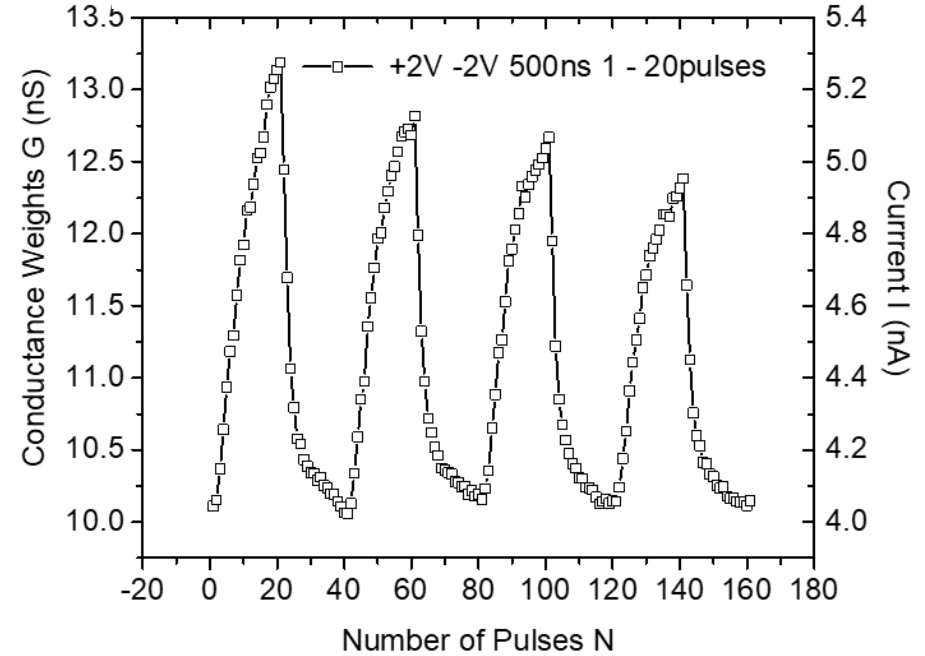
Also: M. Halter et al., *Commun. Mater* **4**, 14 (2023)

Weight update- Identical pulses

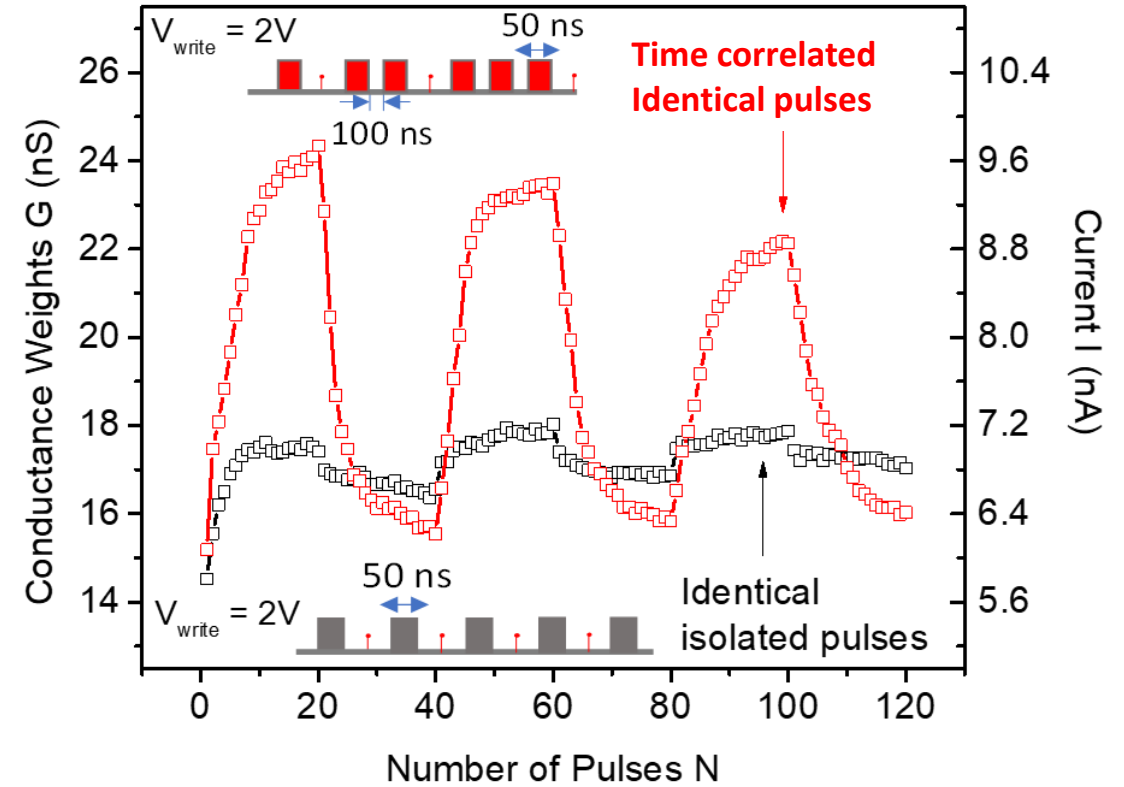
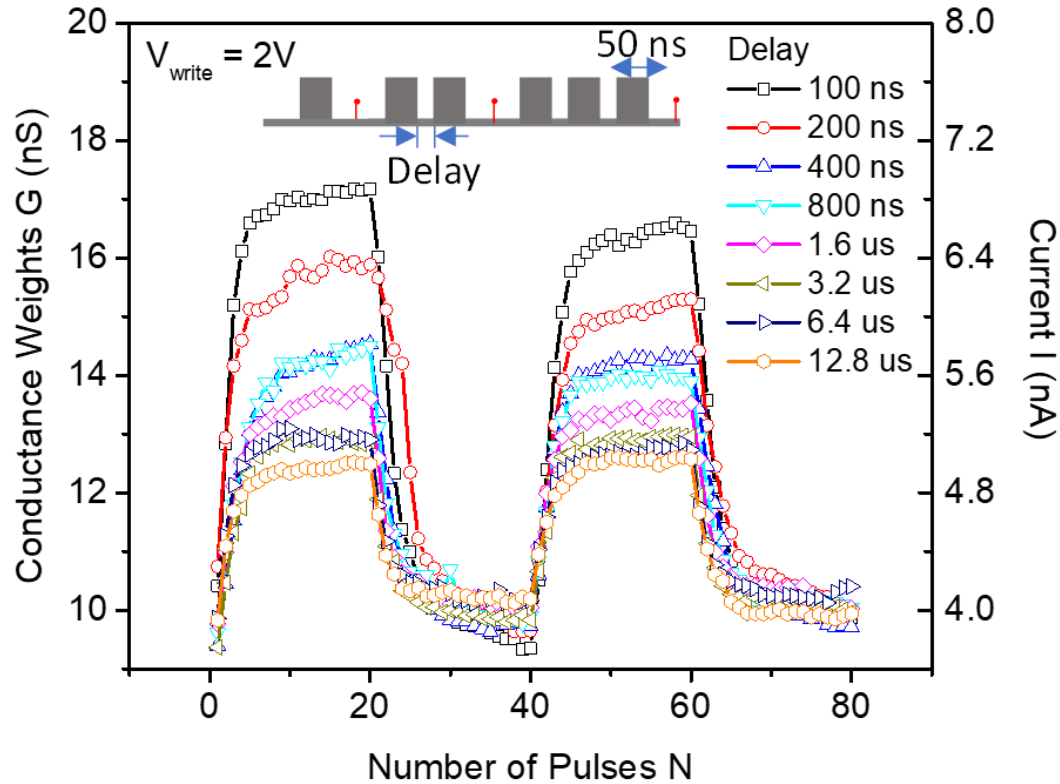
Short delay-time correlated



Nearly ideal (linear & symmetric)

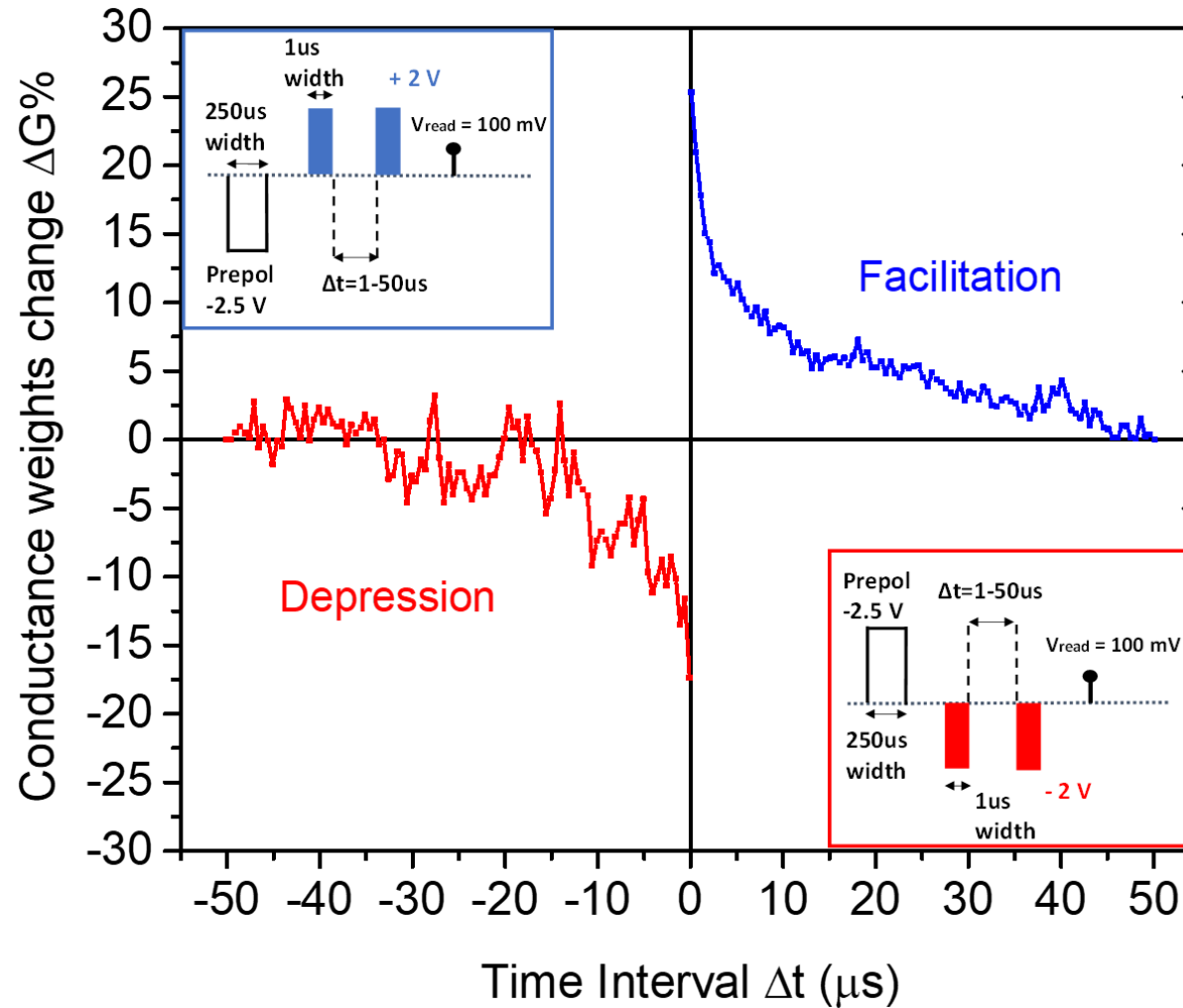


Time correlated vs isolated pulses



Synapse potentiation and depression is only possible with time correlated identical pulses (short delay)

Paired Pulse Facilitation

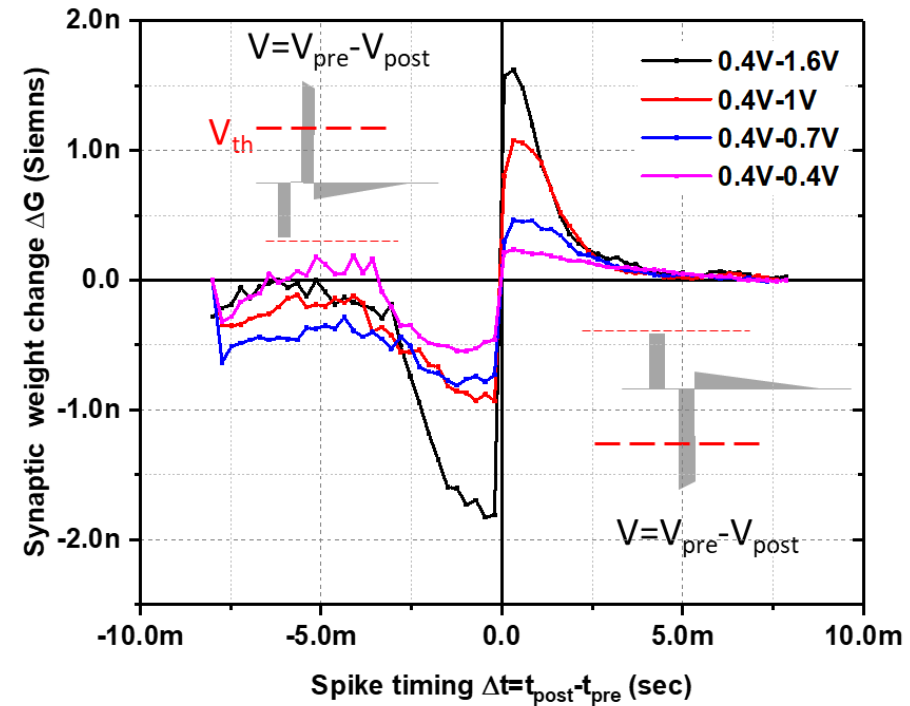
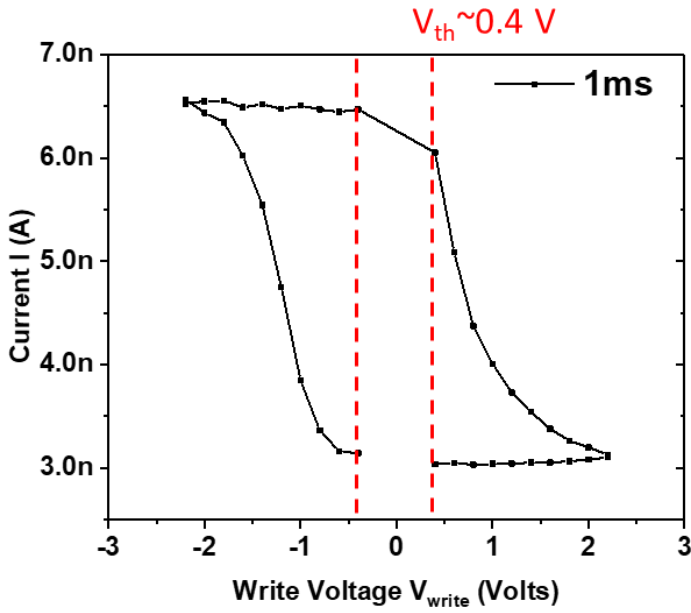
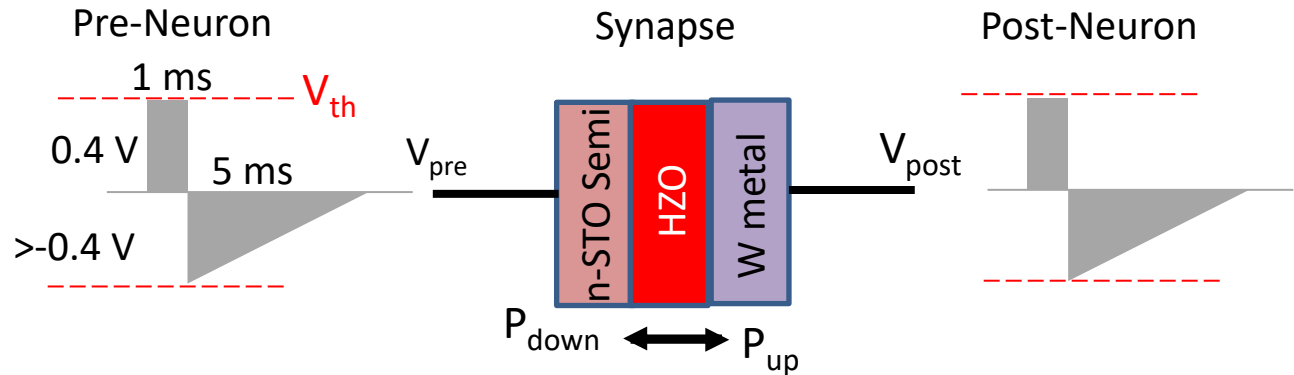


Short term plasticity

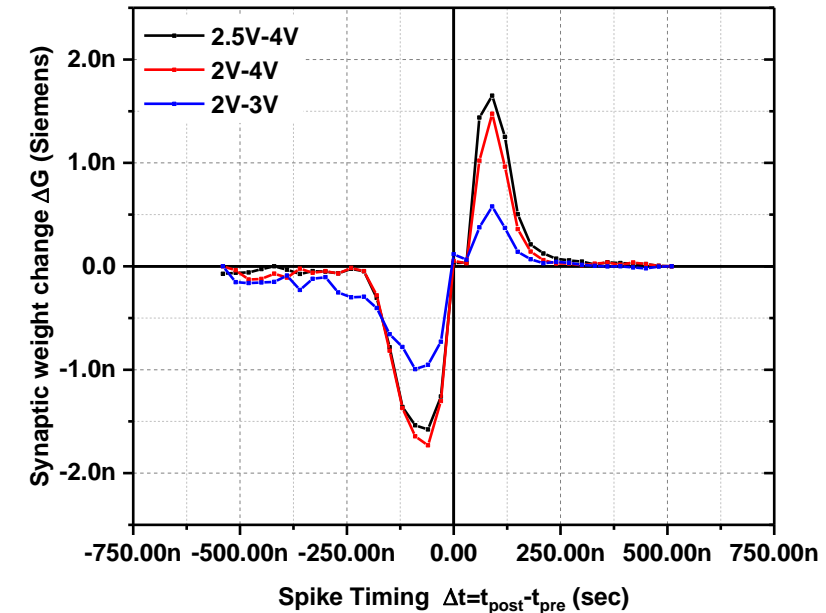
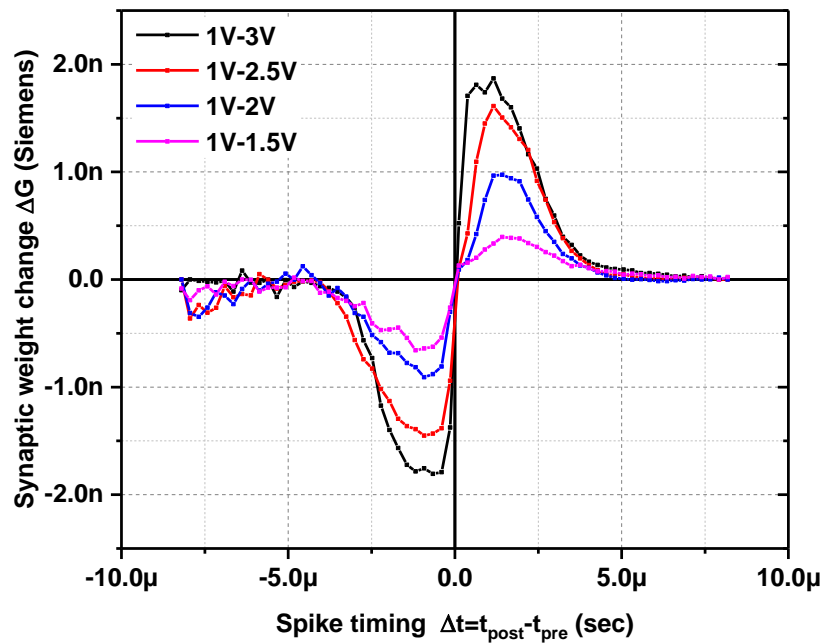
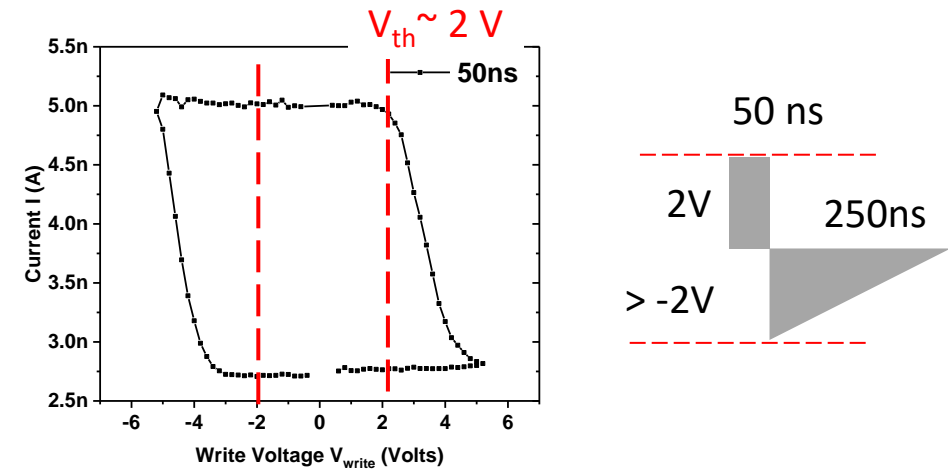
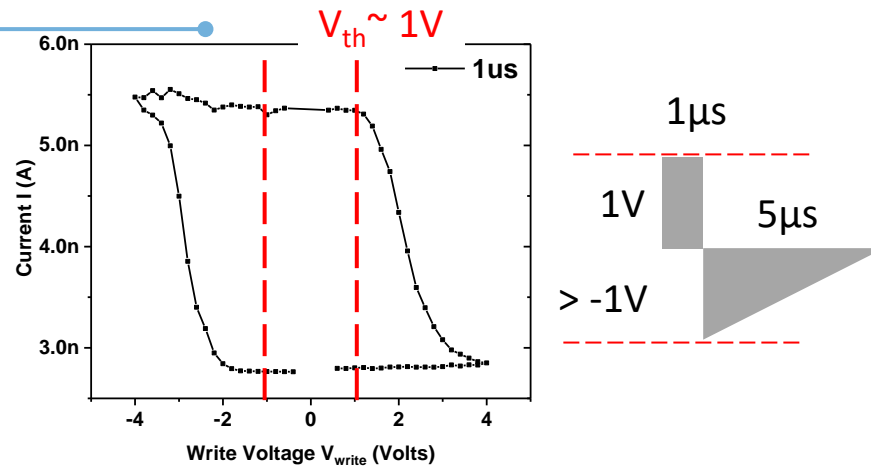
Decode temporary audio/visual info in biological systems

Hebbian learning in biological systems

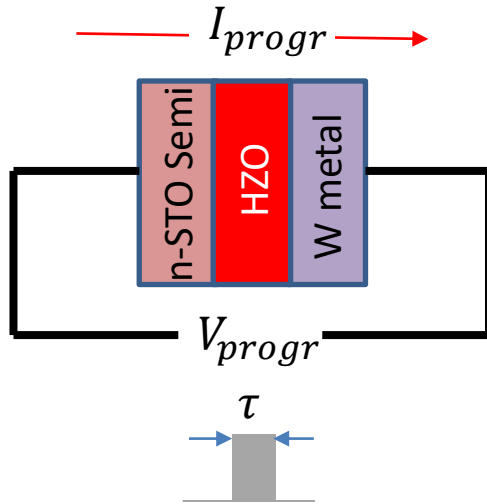
Spiking Neural Networks



STDP-shorter time scales



Energy per programming event



Current driven (RRAM, PCRAM, bio)
Energy spent as Joule heating

$$E = V_{progr} I_{progr} \tau$$

Voltage driven (FE)
Energy to charge a capacitor

$$E = QV = 2PAV$$

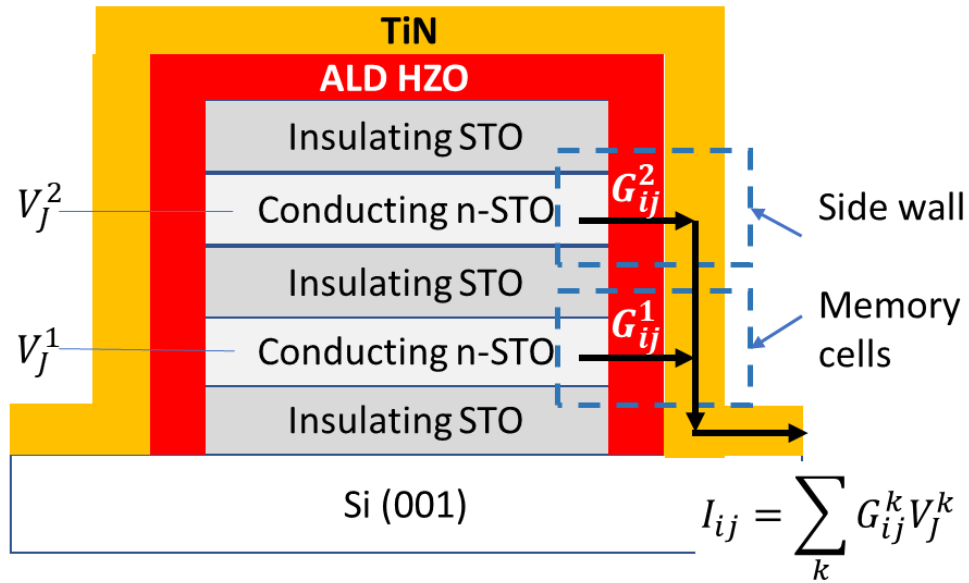
$$P = 20 \mu\text{C}/\text{cm}^2$$

$$V = 2-4 \text{ V}$$

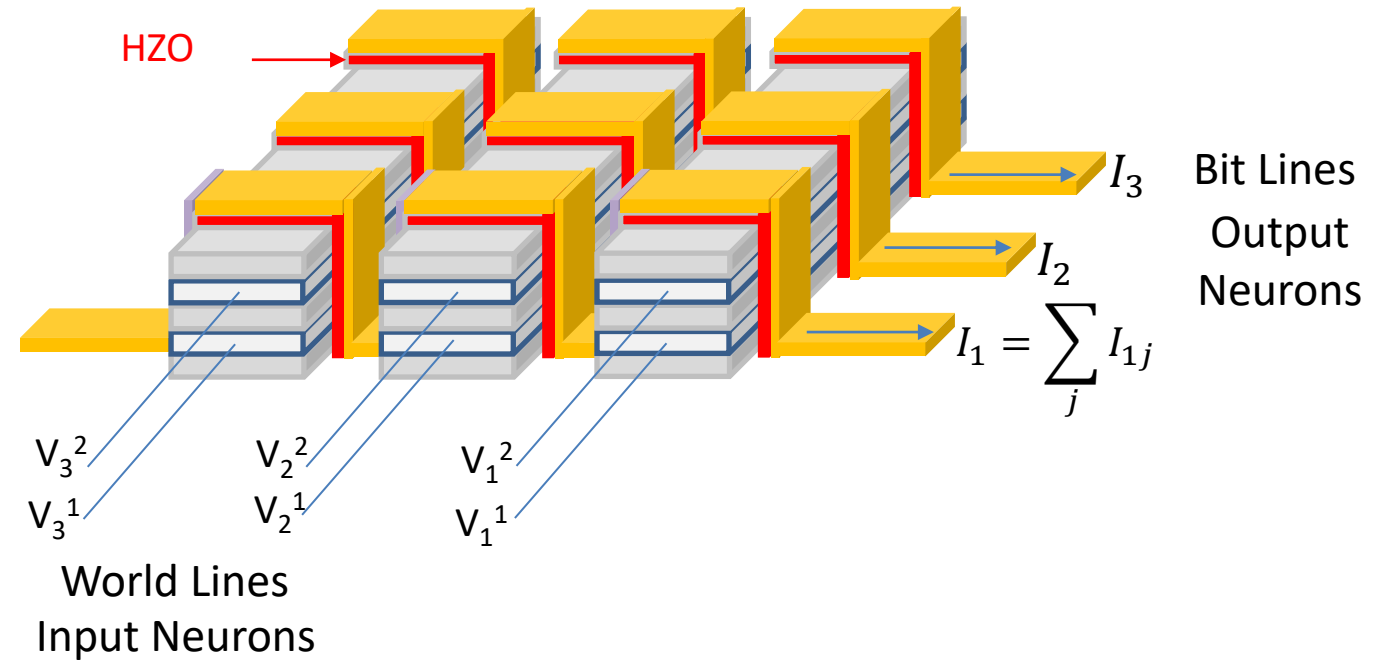
$$A = 100 \times 100 \text{ nm}$$

	V_{prog}	I_{progr}	Pulse width τ	Energy E (Joule heat)	Energy (charging)	Area
Biological	10 mV	1 nA	1 ms	10 fJ		
RRAM	1V	10 μA	100 ns	1 pJ		
PCMRAM				10 pJ		
FTJ (our work)	2V	0.1 pA	1 ms	0.1 fJ	8 fJ	(100x100 nm)

“Multi-floor” Synapses



3D AI accelerator cross bar array



Highly resistive memristor → suitable for large and dense AI accelerator arrays
 Low total power consumption / Minimization of sneak paths



BEOL technology platform based on ferroelectric synaptic devices for advanced neuromorphic processors

Work continues in HORIZON CL4 projects



Scaled Ferroelectric X-bars for AI-driven sensors and actuators



Crystalline Oxides for Next Generation Computing and Emerging Photonic Technologies



Artificial synapses based on ferroelectric tunnel junctions for neuromorphic and analogue computing

Romanian Recovery
and Resilience Plan
“ARSYF”



THANK YOU



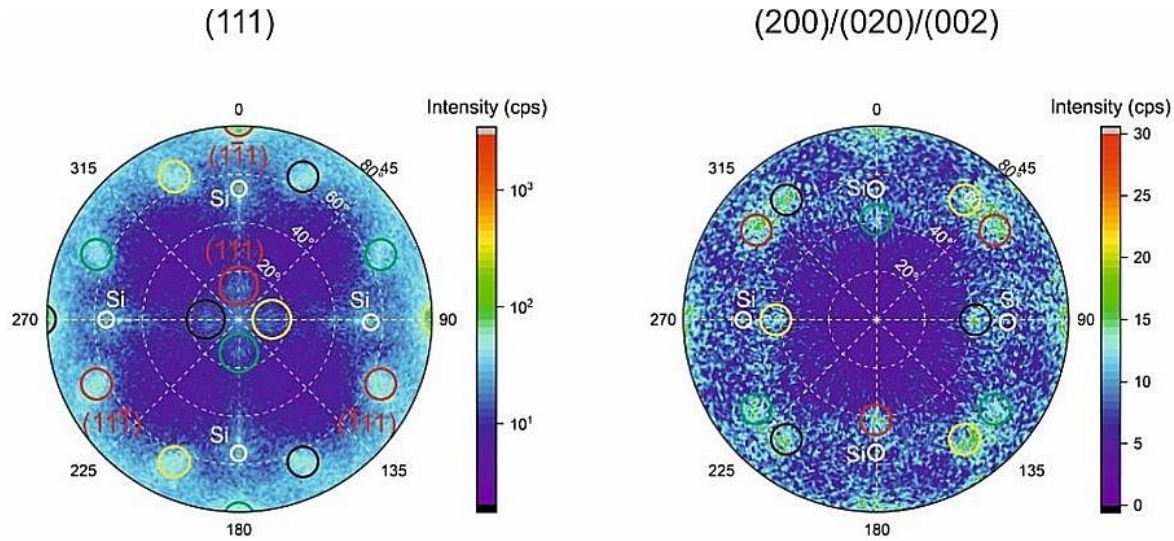
This project has received funding from the European Union's Horizon Europe research and innovation programme under GA N° 101092562

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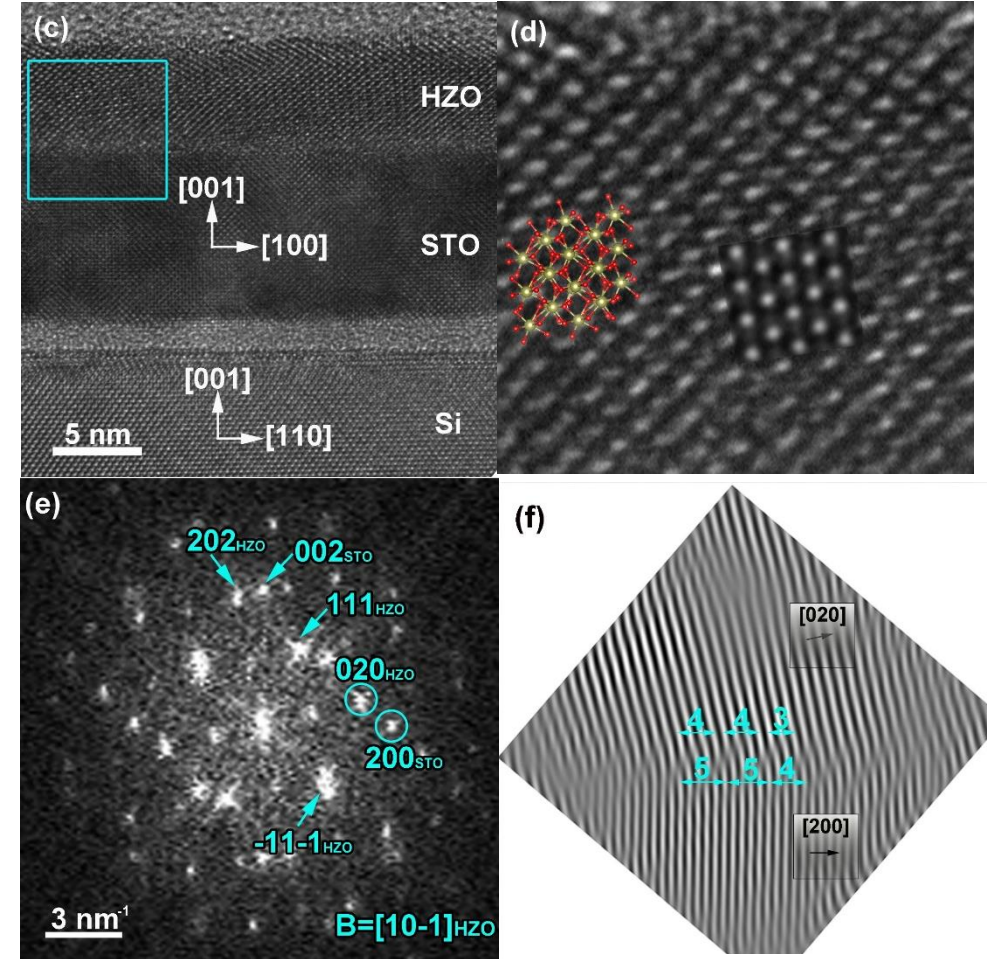
www.icos-semiconductors.eu

Supporting Files

XRD pole figures



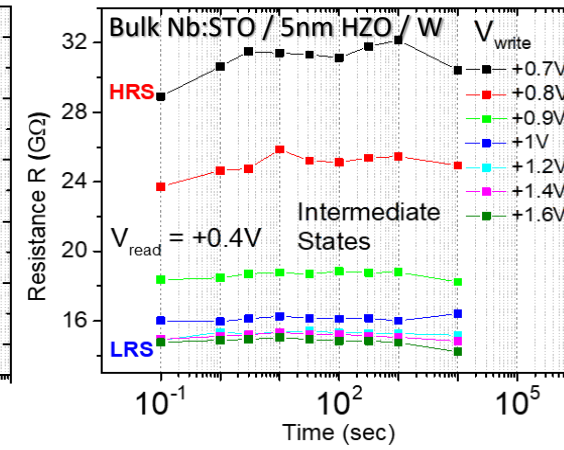
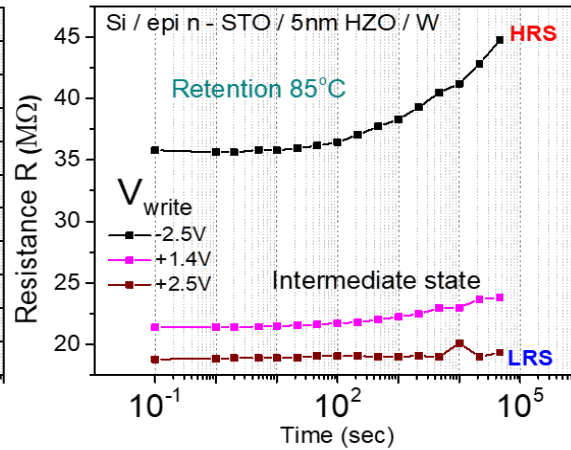
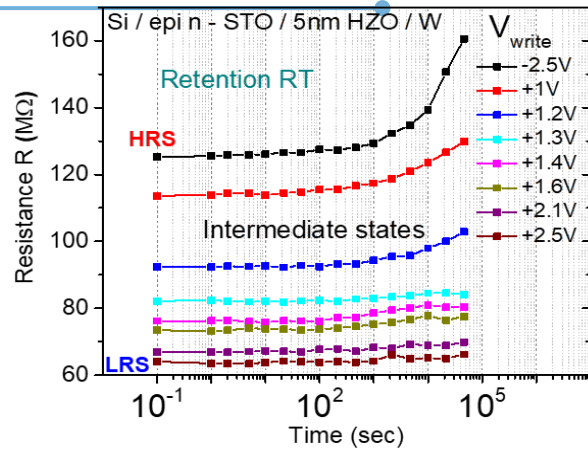
- $[111]$ HZO 15 deg off vertical
- 0, 90, 180, 270 deg rotated domains due to 4-fold symmetry of STO substrate



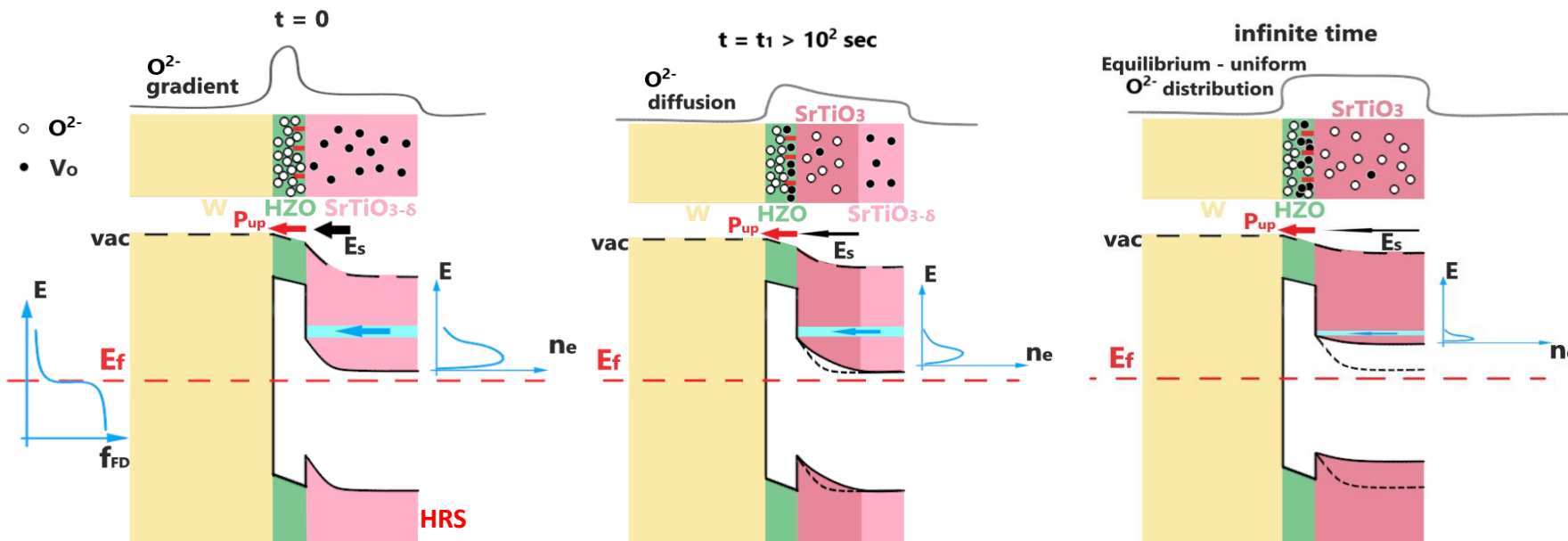
Domain Matching Epitaxy

S. Estandia et al., *Cryst. Growth Des.* **20**, 3801 (2020)

Retention-ionic effects

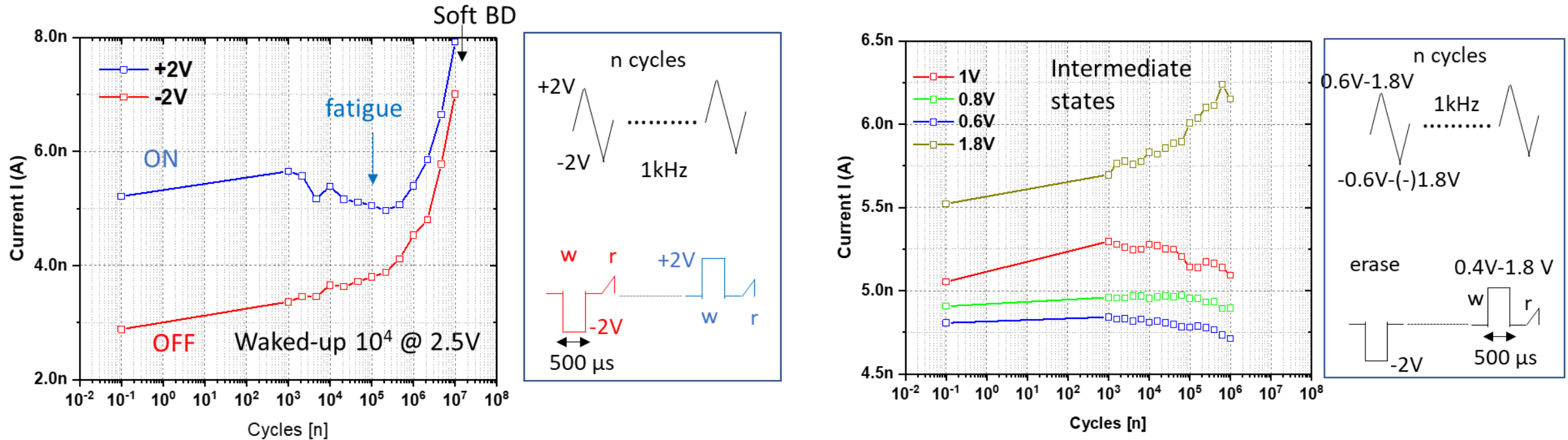


- HRS drifts to larger resistance
- Memory window increases at longer times !



- O^{2-} acceptors diffuse inside STO and compensate the V_o donors
- STO becomes an intrinsic semiconductor increasing its resistance
- Ionic effects occur at shorter timescales too (< 1 ms)

ON, OFF and intermediate states



- Fatigue : appears in devices requiring large wake-up / correlated with Pr fatigue
- Soft breakdown: related to large cycling bias (>2V)
- Intermediate states cycled and read at low V < 1V) show better endurance