

WORKSHOP – Sustainable Electronics & International Cooperation On Semiconductors



Stephan Suckow Group Leader Nanophotonics AMO GmbH



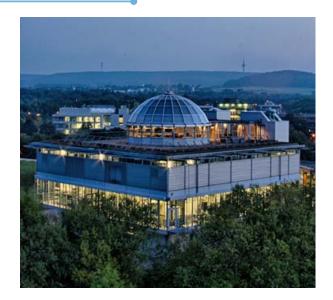
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Grenoble (France)

April 26-28, 2023



AMO Company Profile



Gesellschaft für Angewandte Mikro- und Optoelektronik mbH

Managing Directors:

- Prof. Max Lemme
- Dr. Michael Hornung

- High-Tech SME (non-profit)
- Research Foundry
- Close ties to RWTH Aachen University
- operating since 1997
- 80 staff members
- Applications
 - Nanoelectronics
 - Nanophotonics
 - Integrated Sensors
 - Quantum photonics
- Key technologies
 - Nanofabrication (Stepper, E-Beam, IL, NIL)
 - Silicon technology base
 - 400 m² "extended CMOS" clean room





Why neuromorphic?

- If you have a hammer as good as AI, everything looks like a nail [1]
 - ChatGPT: training GPT-3 consumed 1,287 MWh = 552 t of CO₂ [2]
 - Efficient car: driving 5.5 million km = 137 times around the globe
 - Average yearly CO₂ emissions of 50 Danes [3]
 - Using ChatGPT (BLOOM): 3.6 Wh = 1.5 g CO₂ per request [4]
- Brain: massively parallel neuro-computer at 20 W

[4] <u>https://arxiv.org/pdf/2211.02001.pdf</u>



^[1] https://en.wikipedia.org/wiki/Law_of_the_instrument

^[2] https://arxiv.org/ftp/arxiv/papers/2204/2204.05149.pdf

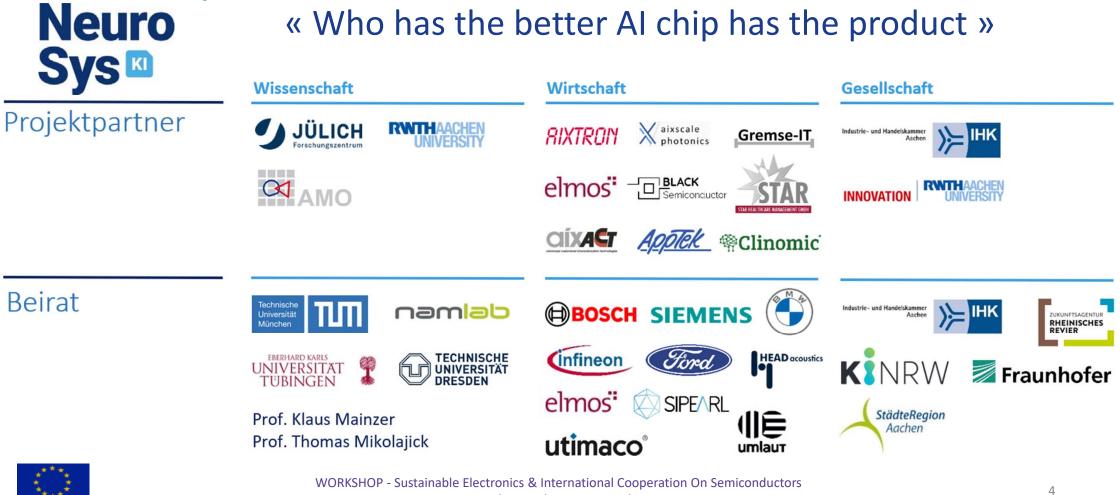
^{[3] &}lt;u>https://kefm.dk/aktuelt/nyheder/2021/apr/foerste-officielle-vurdering-af-danmarks-globale-klimaaftryk</u>

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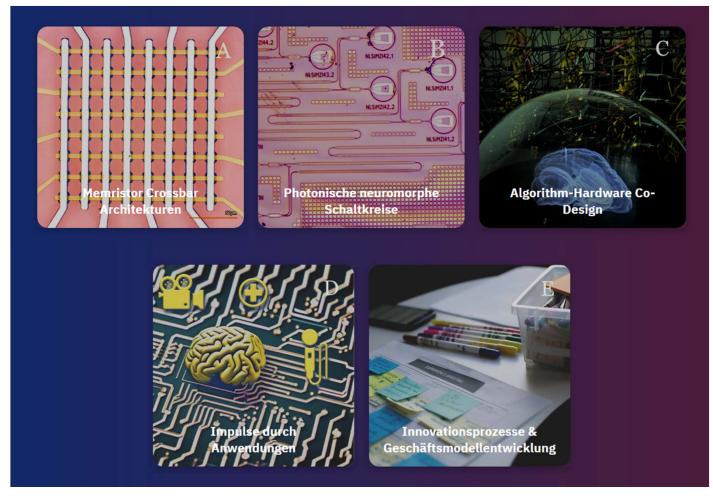
NeuroSys Cluster







NeuroSys Cluster







Neuromorphic hardware

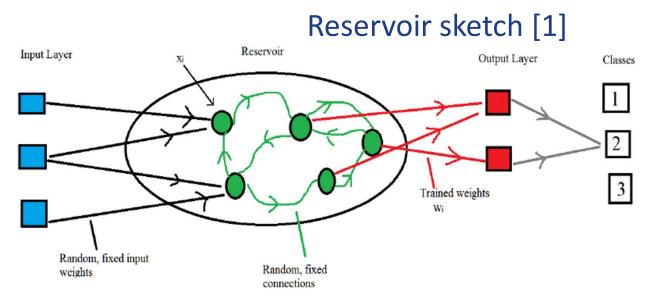
Von Neumann		Power efficiency	Neuromorphic
CPU	GPU	ASIC	memristors brain
		Photonic calculations	Photonic networks
			Memristive elements 1. redox-active materials 2. Phase change materials
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Photonic Reservoir Computing

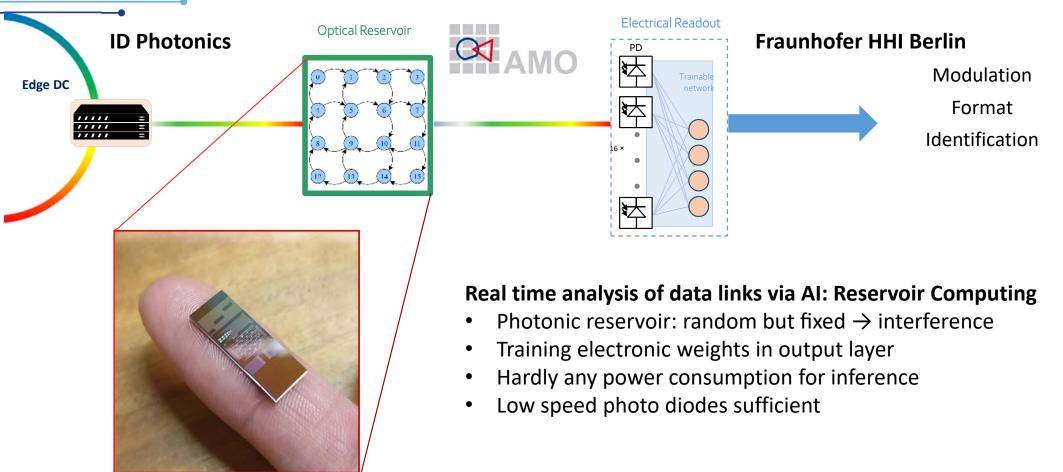


- Form of recurrent neural networks
- « fixed but random » passive reservoir with nonlinearities
- Training only output layer

[1] https://golden.com/wiki/Reservoir_computing-3AMPNA

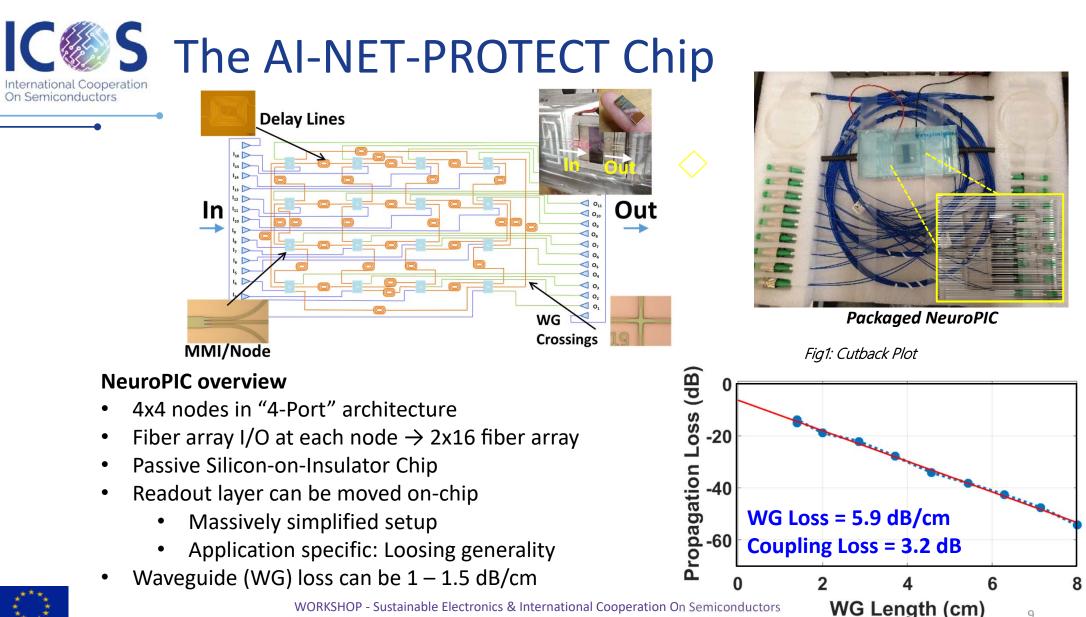


Overview project AI-NET-PROTECT





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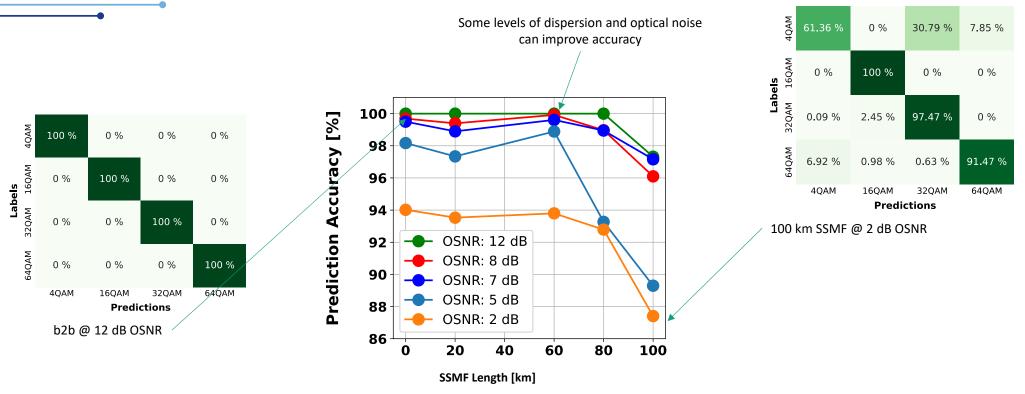


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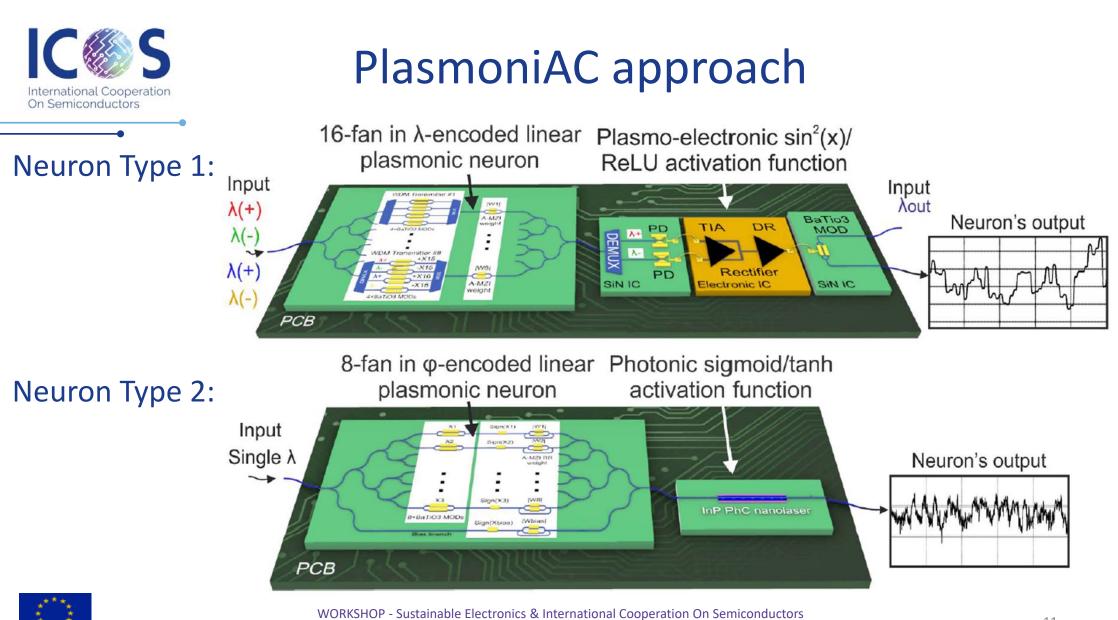


NeuroPIC results and perspective



- Very high classification accuracy achieved → fault tolerant "fixed but random" network
- Output layer can be moved on-chip → merging photonics and electronics
- Method suitable for other tasks with high speed dynamics → looking for follow-up activity





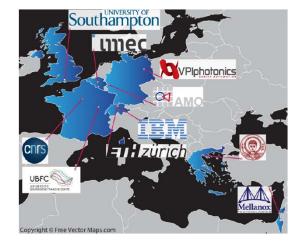
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PlasmoniAC overview

- High risk
 - Combining photonics and plasmonics
 - Hetero-integration on common silicon nitride platform (Southampton)
 - SiOxNy thermo-optic phase shifters (CNRS)
 - InP laser (III-V Labs)
 - BTO phase shifters & 100 GHz modulators (Lumiphase)
 - 100 GHz Graphene photodetectors (AMO, AUTH Uni Thessaloniki)
 - Memristors (IBM)
 - 100 GHz electronics: TIA & modulator driver (IMEC, Mellanox)
- High gain: artificial plasmonic neurons with **1 to 6 orders of magnitude** better energy and footprint efficiencies







Outlook

- Integration complexity extremely challenging
 - « One technology to rule them all » to simplify?
 - « best of all worlds » for best performance?
- Co-integration with electronics, 100+ GHz
 - Photonics & plasmonics in CMOS BEOL
- AMO recent Spin-Off Black Semiconductor:
 - Targetting just that with graphene







Acknowledgements

GEFÖRDERT VOM



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THANK YOU









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